

Candidate for Chair

Jeffrey K. Hollingsworth
University of Maryland, College Park, MD, USA

BIOGRAPHY

Academic Background:

Ph.D., University of Wisconsin, 1994, Computer Sciences.

Professional Experience:

Professor, University of Maryland, College Park, MD, 2006 – Present;
Associate /Assistant Professor, University of Maryland, College Park, MD, 1994 – 2006;
Visiting Scientist, IBM, TJ Watson Research Center, 2000 – 2001.

Professional Interest:

Parallel Computing; Performance Measurement Tools; Auto-tuning; Binary Instrumentation;
Computer Security.

ACM Activities:

Vice Chair, SIGHPC, 2011 – Present;
Member, ACM Committee on Future of SIGs, 2014 – 2015;
General Chair, SC Conference, 2012;
Steering Committee Member (and Chair '13), SC Conference, 2009 – 2014.

Membership and Offices in Related Organizations:

Editor in Chief, PARCO, 2009 – Present;
Program Chair, IPDPS, 2016;
Board Member, Computing Research Association (CRA), 2008 – 2010.

Awards Received:

IPDPS, Best Paper - Software, 2011;
SC, Best Paper - Student lead Author, 2005;
IPDPS, Best Paper - Software, 2000.

STATEMENT

There is a critical need for a strong SIGHPC. Until SIGHPC was founded five years ago, the members of the HPC community lacked a home within ACM. While the SC conference provides a nexus for HPC activity in the fall, we needed a full-time community. Now that the initial transition of the SC conference from SIGARCH to SIGHPC is nearly complete, the time is right to start considering what other meetings and activities SIGHPC should undertake. We need to carefully identify meetings to sponsor or co-sponsor. Also, SIGHPC needs

to continue and expand its role as a voice for the HPC community. This includes mentoring young professionals, actively identifying and nominating members for society wide awards, and speaking for the HPC community within both the scientific community and society at large. I feel my experience in helping to organize the SIG, I was one of the people who helped propose the SIG and served as the initial Vice-Chair, provides me with the vision and experience to carry forward these goals.

Candidate for Vice-Chair

John West

Texas Advanced Computing Center, University of Texas at Austin, TX, USA

BIOGRAPHY

Academic Background:

M.S., Mississippi State University, 1994, Computational Engineering.

Professional Experience:

Director of Strategic Initiatives, Texas Advanced Computing Center, University of Texas at Austin, TX, 2014 – Present;

Director, DoD HPC Modernization Program, Vicksburg, MS, 2011 – 2014;

Special Asst. to the Director for HPC, Engineer Research and Development Center, ITL, Vicksburg, MS, 2009 – 2011.

Professional Interest:

High performance computing; Visualization; Applied computing.

ACM Activities:

Executive Committee (Communications and Advisory Board), SIGHPC, 2011 – Present;

General Chair, SC16, 2015 – Present;

Communications Chair, SC13, 2012 – 2013.

Membership and Offices in Related Organizations:

Associate Editor, IEEE/AIP Computing in Science and Engineering, 2012 – 2014.

STATEMENT

I have been an active contributor to the volunteer leadership of SIGHPC since its formation, managing some aspects of member communications and serving as the appointed chair of the SIGHPC Advisory Board. HPC as a discipline is relevant to nearly all fields of science and engineering, and as such has a broad impact on society and quality of life. If elected, I will continue to develop SIGHPC's leadership role in organizing and nurturing our inherently multidisciplinary community, and in sponsoring activities that highlight the importance of our field to both the general public and to policy makers.

Candidate for Vice-Chair

James Kenneth Cliburn, Jr.
Secure Mission Solutions (contractor for DoD HPCMP), North Charleston, SC, USA

BIOGRAPHY

Academic Background:

Master of Business Administration, Utah State University, 1997, Business Administration.

Professional Experience:

Technical Adviser and Project Manager, Secure Mission Solutions (contractor for DoD HPCMP), North Charleston, SC, 2012 – Present;

Deputy Program Manager, Lockheed Martin (contractor for DoD HPCMP), Vicksburg, MS, 2008 – 2012;

Site Technical Lead, Computer Sciences Corp. (contractor for DoD HPCMP), Vicksburg, MS, 1999 – 2008.

Professional Interest:

Management of multiple supercomputing centers; Specification of petascale supercomputing platforms; HPC service portfolio refinements amid budgetary constraints; Production of DoD HPCMP semiannual magazine; Standardizing HPC configurations across multiple HPC centers.

ACM Activities:

Infrastructure Chair, SC16, 2015 – Present;

Space Chair, SC15, 2014 – 2015;

AV/PC Chair, SC13, SC14, 2012 – 2014;

SCinet Logistics, SC12, 2012.

STATEMENT

My complete academic background: B.S Physics (Univ of Utah 1987); M.S.C.S. (Utah State Univ 1993); MBA.

I have over 30 years experience in technical leadership of Department of Defense computing; 18 of those years in affiliation with the DoD HPC Modernization Program. I am a former US Navy submariner and retired Naval Reserve Engineering Duty Officer. I have four years as Supercomputing committee member of increasing responsibility.

I appreciate the opportunity to apply for the SIGHPC Vice-Chair position -- a position where my technical and leadership skills, discipline, and attention to detail might be best utilized to enable the Chair to formulate, articulate, and attain SIGHPC goals and objectives.

Candidate for Treasurer

Wilfred Pinfeld
Portland State University, Portland, OR, USA
URBAN.SYSTEMS, Portland, OR, USA

BIOGRAPHY

Academic Background:

Ph.D., University of Strathclyde, 1985, Computational Fluid Dynamics.

Professional Experience:

CEO, URBAN.SYSTEMS, Portland, OR, 2015 – Present;
Director, Research and Advanced Development, Intel Federal, Hillsboro, OR, 2013 – 2015;
Director, Extreme Scale Programs, Intel Corporation, Hillsboro, OR, 2008 – 2013.

Professional Interest:

Smart City and Internet of Things; Reproducibility of Scientific Research and Publication;
Exascale System Development; Computational Science and Big Data Analytics;
Additive and 3D Manufacturing.

ACM Activities:

SC Conference Steering Committee, SIGHPC, 2014 – Present;
SC Conference HPC Matters Chair, SIGHPC, 2014 – Present;
Advisory Board, SIGHPC, 2011 – 2014;
SC Conference General Chair, SIGARCH, 2007 – 2009.

STATEMENT

I have been an active member of SIGHPC since its inception. I was the first Chair of the Advisory Board and served for three years. In that role I helped found the first chapter (education) and supported the SIG through its rapid growth to the vibrant organization it is today. I have been an active volunteer in the SC conference since 1990 when I served on the technical review committee. I held a variety of technical and administrative roles before being selected as Chair for the very successful 2009 conference in Portland. I currently serve on the conference steering committee. If elected, I will work to ensure responsible and effective management of SIGHPC finances. I will use my industry colleagues to build on recent development successes like the ACM SIGHPC/Intel Computational and Data Science Fellowships. I will work closely with other executive committee members and continue the excellent work done building chapters with particular attention to diversity and internationalization. I will support programs that reinforce the role of High Performance Computers in science, medicine, engineering, technology, and industry. I will work with our membership to make SIGHPC a platform for collaboration and professional advancement.

Candidate for Treasurer

Rajeev Thakur
Argonne National Laboratory, Argonne, IL, USA

BIOGRAPHY

Academic Background:

Ph.D., Syracuse University, 1995, Computer Engineering.

Professional Experience:

Deputy Division Director, Argonne National Laboratory, Argonne, IL, 2012 – Present;
Senior Computer Scientist, Argonne National Laboratory, Argonne, IL, 2011 – Present;
Computer Scientist, Argonne National Laboratory, Argonne, IL, 2002 – 2011.

Professional Interest:

High-performance computing; Parallel programming models; Runtime systems;
Communication libraries; Scalable parallel I/O.

ACM Activities:

Member-at-Large, SIGHPC, 2013 – Present.

Membership and Offices in Related Organizations:

Steering Committee Member, SC Conference, 2015 – Present;
Technical Program Chair, SC12 Conference, 2012;
Associate Editor, IEEE Transactions on Parallel and Distributed Systems, 2004 – 2007.

Awards Received:

R&D 100 Award, 2005.

STATEMENT

Having served as an elected Member-at-Large of SIGHPC for the past three years, I am very familiar with the functioning of SIGHPC and its various activities. I would like to serve as Treasurer of SIGHPC for the next three years to ensure that SIGHPC continues to be financially strong and meets the needs of its members. I have worked in the HPC field for the past 20 years in various capacities. I have also served on the committees of several HPC conferences, particularly the SC conference series for which I serve on the Steering Committee and was technical program chair for SC12. I have close ties to a broad spectrum of people in the HPC community in academia, industry, and research laboratories in the US, Europe, and Asia. I hope to use my background, experience, and connections with the community to ensure that the community's interests are well served by ACM SIGHPC.

Candidate for Member-at-Large

Anne Fitzpatrick
US Department of Defense, Washington, DC, USA

BIOGRAPHY

Academic Background:

Ph.D., Virginia Tech, 1998, Science and Technology Studies.

Professional Experience:

Associate Director for Strategic Initiatives, US Department of Defense HPC Modernization Program, Washington, DC, 2015 – Present;
Chief Scientist for Cyber, US Department of Energy (DOE), Washington, DC, 2014 – 2015;
Deputy Director for Future Computing, US Department of Defense (DOD), Washington, DC, 2012 – 2014.

Professional Interest:

HPC Future Trends Research; Non-CMOS Architecture; International trends in HPC; Advocacy for HPC adoption; General Science and Technology Policy.

ACM Activities:

Member, ACM, 2012 – Present.

Membership and Offices in Related Organizations:

Member at Large, Societal Impacts of Science and Technology, American Association for the Advancement of Science, 2010 – Present;
Deputy Editor, IEEE Annals of the History of Computing, IEEE, 2003 – 2005.

Awards Received:

National Security Agency Director's Award, 2014;
US DOE Leadership Award, 2012;
Northrop Grumman Corporation Timely Award, 2007.

STATEMENT

I have 23 years of hands on experience in science and technology and HPC forecasting analyses, strategic planning, and measuring value for high performance computing, and cyber innovation efforts. The result has been the creation, development, and implementation of successful, economical, and creative solutions to the Department of Defense, Department of Energy, US Intelligence Community, White House, and US Congressional policy formulation. I have directed and participated in international collaborative research to measure US capabilities against competitors, and support arms control measures. Have extensive

experience working individually and within a team environment, and making presentations to Cabinet level leadership. As SIGHPC Member at Large I would work to effectively bridge communications between technical and non-technical personnel and across government agencies, and filling knowledge gaps between scientific and policy making bodies. I will also advocate the of adoption of high performance computing by a wide community, as well as provide the intellectual and philosophical arguments and justification to push forward a broad array of new technical initiatives in HPC.

Candidate for Member-at-Large

Suzanne K. McIntosh
Cloudera, Inc., New York, NY, USA
New York University, New York, NY, USA

BIOGRAPHY

Academic Background:

Engineer Degree (post M.S.), Stevens Institute of Technology, 2000, Computer Engineering.

Professional Experience:

Solution Engineering and Training, Cloudera, Inc., New York, NY, 2013 – Present;
Adjunct Professor, Big Data and Analytics, New York University-Courant Institute of Mathematical Sciences, New York, NY, 2013 – Present;
Senior Research Software Engineer, Team Lead, IBM T. J. Watson Research Center, Yorktown, NY, 2004 – 2013.

Professional Interest:

HPC and Distributed Architectures; Hadoop and Spark Ecosystem; Secure Software Engineering; Cloud and Virtualization Technologies; Realtime Embedded Systems.

ACM Activities:

Member-at-Large and Founder, ACM SIGHPC-BigData, 2015 – Present;
Membership Chair, ACM SIGHPC, 2014 – Present;
Program Committee Member, Supercomputing (SC), 2014 – Present.

Membership and Offices in Related Organizations:

Program Committee Chair, International Big Data Congress, IEEE, 2014 – Present;
Guest Editor, Transactions on Services Computing, IEEE, 2014 – Present;
Webmaster and Employment Network Chair, IEEE North Jersey Section, 2011 – 2014.

Awards Received:

IBM Research-Technical Accomplishment Awards, Invention Plateaus, 2013;
ACM Senior Member, 2012;
Best Paper, International Conference on Autonomic Computing-ICAC, 2011;
IEEE Senior Member, 2011.

STATEMENT

I am honored by the nomination to represent the SIGHPC community as Member-at-Large. I have been serving on the SIGHPC executive committee as Membership Chair since 2014 and have enjoyed working with our dedicated officers and meeting fellow SIGHPC members at our Supercomputing booth each year. To all of you who volunteer your time to staff the booth, my sincere gratitude.

The main duties of the SIGHPC Member-at-Large are to be the voice of the community in SIG decisions and to participate in working groups that will continue to move our SIG forward. I am asking for your vote because I am committed to giving voice to your suggestions, I understand how SIGHPC functions, I am familiar with the issues at hand, and I am ready to work on your behalf to grow SIGHPC in every way.

I am particularly interested in growing our community by increasing the number of SIGHPC virtual chapters. Currently, we have three virtual chapters:

- SIGHPC – Big Data,
- SIGHPC - Education, and
- SIGHPC – Resource Constrained Environments.

Our virtual chapters provide opportunities for members to evolve, innovate, and share best practices in focused areas of common interest.

Candidate for Member-at-Large

Michela Taufer
University of Delaware, Newark, DE, USA

BIOGRAPHY

Academic Background:

Ph.D., Swiss Federal Institute of Technology in Zurich (ETHZ), 2002, Computer Science.

Professional Experience:

Acting Director at Center for Bioinformatics & Computational Biology (CBCB), Delaware Biotechnology Institute (DBI), Newark, DE, 2015 – 2016;
Associate Professor in Computer and Information Sciences, University of Delaware, Newark, DE, 2012 – Present;
Assistant Professor in Computer and Information Sciences, University of Delaware, Newark, DE, 2007 – 2012.

Professional Interest:

High-performance computing; Scientific applications; Performance analysis, modeling, and optimization; Cloud computing and volunteer computing; Big data analytics.

ACM Activities:

Panel Chair, ACM/IEEE Supercomputing Conference 2016, 2015 – Present;
Workshop Chair, ACM/IEEE Supercomputing Conference 2015;
Technical Program Co-Chair, ACM HPDC Conference 2015;
Technical Program Co-Chair, ACM/IEEE Supercomputing Conference 2015, 2014.

Membership and Offices in Related Organizations:

Steering Committee Member, ACM/IEEE Supercomputing Conference, 2015 – Present;
Webmaster, SIGHPC, 2015 – Present.

Awards Received:

ACM Distinguished Scientist, 2015;
ACM Senior Member, 2014.

STATEMENT

The SIGHPC is a vibrant community. Still we can do more to further promote SIGHPC and to touch a broader, more diverse community. I have been a member of ACM for the past 18 years; I am an active volunteer contributing to the success of SIGHPC for the past two years. I served as Committee Member on various ACM conferences and am a Steering Committee Member of the SC conference. My experience in these positions has prepared me well for serving you, and SIGHPC, as a Member-at-Large.

If you choose me, I will continue the tremendous effort and high value activities that the current SIGHPC leadership built in the past years, together with the several SIGHPC volunteers and the SIGHPC community. Moreover, I will reach out to the new generation of SIGHCP volunteers by promoting the active participation of junior faculty, post-docs, and researchers in the SIGHPC community. I also want to support the building of a more diverse SIGHPC community by including women as well as small colleges and university.

As a Member-at-Large, I am committed to work with you to make SIGHPC your home base for your educational and research activities.

Candidate for Member-at-Large

Torsten Hoefler
ETH Zurich, Switzerland

BIOGRAPHY

Academic Background:

Ph.D., Indiana University, 2008, HPC/Computer Science.

Professional Experience:

Assistant Professor, ETH Zurich, Switzerland, 2012 – Present;

Adjunct Assistant Professor, University of Illinois at Urbana-Champaign, Champaign, IL, 2010 – 2013;

Performance Modeling and Simulation Lead, Blue Waters Directorate/NCSA, Champaign, IL, 2009 – 2012.

Professional Interest:

High-performance Computing; High-performance Systems; Performance Modeling; Parallel Programming; Benchmarking.

ACM Activities:

Member at Large, SIGHPC, 2013 – Present.

Awards Received:

Latsis Prize at ETH Zurich, 2015;

Indiana University Young Alumni Award, 2014;

IEEE/TCSC Young Achiever in Scalable Computing, 2013;

SIAM SIAG/SC Junior Scientist Prize, 2012.

STATEMENT

I view SIGHPC as one of the most important organizations to support the HPC community. As the co-chair of the yearly ROSS workshop series (2012-present), I initiated the collaboration with SIGHPC shortly after it was founded. This made ROSS'12 the first workshop in collaboration with SIGHPC. It was a big success and has been featured in newsletters and other community news. I also encouraged the PASC conference to seek SIGHPC sponsoring to provide a strong base for SIGHPC in Europe. PASC is, after SC, the second conference sponsored by SIGHPC.

I will contribute with my service as a Member at Large on the SIGHPC Executive Committee to further strengthen our growing scientific community. I am slightly concerned about the

increasing costs of some conferences and will support the expansion of student travel grants and competitions. For example, I was chair of the ACM student research competition at SC12 and found my experience with young and bright graduate and undergraduate researchers enlightening. It is important to support young scientists entering our field.

Finally, as an active member of the HPC community, I will ensure that SIGHPC, when appropriate, speaks on behalf of this community and supports its needs.

Candidate for Member-at-Large

Raghunath Raja Chandrasekar
Cray Inc., Austin, TX, USA

BIOGRAPHY

Academic Background:

Ph.D., The Ohio State University, 2014, Computer Science and Engineering.

Professional Experience:

Software Engineer, Cray Inc., Austin, TX, 2015 – Present;
Graduate Research Associate, The Ohio State University, Columbus, OH, 2009 – 2014;
Summer Research Scholar, Lawrence Livermore National Laboratory, Livermore, CA,
Summer 2012, 2013 – Fall 2012, 2013.

Professional Interest:

High-Performance Computing; Storage and I/O; Distributed Computing; Fault-Tolerance;
System Software.

ACM Activities:

Tech. Program Committee Member, ACM/IEEE Supercomputing Conference, 2015 – Present;
External Reviewer, Several ACM and IEEE conferences and journals, 2011 – Present.

Membership and Offices in Related Organizations:

Member, IEEE, 2010 – Present;
Member, IEEE Computer Society, 2010 – Present;
Technical Program Committee Member, FTS workshop at IEEE Cluster, 2015 – Present.

Awards Received:

ACM HPDC Best Poster Award, 2014;
ACM HPDC Best Presentation Finalist, 2013;
5+ ACM, USENIX, and IEEE travel awards (2011-2014), 2011.

STATEMENT

I am thrilled to run for the Member-at-Large office of SIGHPC this year. I have had the opportunity to witness the evolution of HPC over the past decade from various vantage points - as an undergraduate student, as a PhD candidate working on challenging problems, as an intern at LLNL, as an employee at Cray, and more importantly, as an enthusiast. It constantly amazes me how vendors, labs, universities, scientists, and governments, work together to address the common challenges facing us as we strive to build larger, faster,

and more efficient supercomputing systems. Special Interests Groups like SIGHPC help facilitate this conversation, and I can not be more happier to represent the community by running for this office.

As my CV would elaborate, I have served on Technical Program Committees, as an external reviewer for several conferences and journals, and have helped judge research in different capacities. I actively publish my research in leading HPC conferences and closely watch the HPC industry trends. More importantly, as a young scientist in HPC, I am keen on giving back to the community, and I feel volunteering my time to SIGHPC is great way to do that.