

Candidate for Chair

Frank de Boer
Centrum Wiskunde & Informatica, Amsterdam, Netherlands

BIOGRAPHY

Academic Background:

Ph.D., Free University of Amsterdam, 1991, Proof Theory Object-Oriented Languages.

Professional Experience:

Leader Formal Methods research group, CWI, Amsterdam, 2010 – Present;
Professor, Leiden University, Leiden, Netherlands, 2008 – Present;
Coordinator Theme Software, CWI, Amsterdam, Netherlands, 2011 – Present.

Professional Interest:

Programming logics and semantics; Model-checking; Design and implementation actor-based languages; Runtime verification; Simulation intelligent transport systems.

ACM Activities:

Associate Editor, ACM Transactions on Computational Logic, 2015 – Present.

Membership and Offices in Related Organizations:

Editor in Chief Formal Methods, Frontiers (Open Access journal), 2015 – Present;
Editor Open Access Journal Scientific Programming, Hindawi Publishing Organisation, 2014 – Present;
Treasurer, Dutch org. Theoretical Informatics, 2014 – Present.

STATEMENT

My main overall goal is to increase the general awareness of ACM SIGLOG, specifically attract young people. To this aim I would like to add more dynamic content to the website and initiate activities along the following lines:

Education: Use of the website as a portal for online courses to further advocate the importance of logic in the general computer science curriculum. Also of interest in this context is the addition of information about logical problems (and their possible solutions) At varying degrees of difficulty, ranging from undergraduate level to the level of open scientific problems.

Tools: Provide and maintain a list of state of the art tools for logic, e.g., tools for program verification, theorem provers. A rather ambitious but interesting activity in this context is the organization of tool competitions, and establishing and updating a list of corresponding benchmarks.

Social media: Provide means for direct interactions by use of modern social media. For example, use of the website for blogging about and discussions of logic and computation related topics.

Besides the above, I would like to discuss other possible conferences to sponsor or to support. Finally, I would like to investigate further sponsoring of ACM SIGLOG itself.

Candidate for Chair

Prakash Panangaden
McGill University, Montréal, Québec, Canada

BIOGRAPHY

Academic Background:

Ph.D., University of Wisconsin-Milwaukee, 1980, Physics.

Professional Experience:

Professor, School of Computer Science; McGill University, Montréal, Québec, Canada, 1990 – Present;
Associate Professor, Computer and Information Science, Queen's University, Kingston, Ontario, Canada, 1990;
Assistant Professor, Computer Science Department, Cornell University, Ithaca, New York, 1985 – 1989.

Professional Interest:

Probabilistic systems; Automata theory; Semantics of programming languages; Concurrency theory; Quantum information.

ACM Activities:

Chair, SIGLOG, 2014 – Present.

Membership and Offices in Related Organizations:

Member, LICS Organizing Committee, 2005 – 2008;
Member, MFPS Organizing Committee, 2007 – Present;
Member, LICS Advisory Board, 2011 – 2015.

Awards Received:

Fellow of the Royal Society of Canada, 2013.

STATEMENT

I am pleased at the opportunity to run for the position of SIGLOG Chair in the first election. I have been the Chair since the inception of SIGLOG two years ago and I have seen it grow to its present size of almost 300. My hope is to see SIGLOG through to its next renewal in two years' time and then to hand over the reins. SIGLOG is still in its growth phase and I feel that someone who has experience interacting with ACM would serve the community best in the next two years. We have made a lot of progress: we have cooperation agreements with similar organizations in Europe, we have a flourishing community letter with substantial technical content, we have reached out to conferences and workshops to

affiliate with SIGLOG and we have founded a major award named after Alonzo Church. There is more to be done, of course, and I hope to have your support to put SIGLOG on a firm footing.

Candidate for Chair

Simona Ronchi Della Rocca
University of Torino, Torino, Italy

BIOGRAPHY

Academic Background:

Master Degree, University of Torino, Italy, 1969, Nuclear Physics.

Professional Experience:

Full Professor, University of Torino, Italy, 1987 – Present;
Associated Professor, University of Torino, Italy, 1983 – 1987;
Assistant Professor, University of Torino, Italy, 1973 – 1983.

Professional Interest:

Lambda Calculus; Type Theories; Linear Logic; Implicit Computational Complexity;
Semantics of Programming Languages.

ACM Activities:

Editor of TOCL (ACM Transaction on Computational Logic), 2000 – Present.

Membership and Offices in Related Organizations:

Vice-President, European Association for Computer Science Logic (EACSL),
2012 – Present;
Member, European Association for Theoretical Computer Science (EATCS),
1988 – Present;
Secretary, Italian Chapter of European Association for Theoretical Computer Science
(EATCS), 1988.

STATEMENT

From my point of view, there are two main problems for our community.

Teaching Logic. Many universities have computer science undergraduate curricula without courses in logic, so students start the highest level of studies with quite few competences in logic and formal methods. Logic cannot be taught only at the graduate level: logic is a way of thinking, and needs to be learned at the beginning of the formation.

Research funding. The list of topics of the European calls is more and more moving to applied research; computer science, in particular, is essentially considered as a tool for the applicative part in other research fields. The foundational research is funded essentially by

the local government, with less funds every year since the economical crisis. I do not know the situation in other countries.

SIGLog can play a role in order to solve such problems, by sponsoring public discussions and publishing statements about it. The best would be to enhance the collaboration with EACSL and EATCS. The three institutions could work together to enforce the role of logic both in teaching and research, and to influence the EU policy in financing the research projects in logic and foundation of Computer Science.

Candidate for Chair

Aravinda Prasad Sistla
University of Illinois at Chicago, Chicago, IL USA

BIOGRAPHY

Academic Background:

Ph. D., Harvard University, 1983, Computer Science/Applied Mathematics.

Professional Experience:

Professor, University of Illinois at Chicago, Chicago, IL, 2000 – Present;
Associate Professor, University of Illinois at Chicago, Chicago, IL, 1991 – 2000;
Senior member of Technical Staff, GTE Research Laboratories, Waltham, MA,
1985 – 1991.

Professional Interest:

Formal Methods; Model Checking; Logic in Computer Science; Security; Database Systems.

STATEMENT

If elected as chair, I will strive towards bringing out the importance of Logic and Formal Methods in Computer Science and its advancement. In the area of education, we need to identify and recommend a critical list of topics in Logic and Formal Methods that need to be included in the general undergraduate CS curriculum. A similar list of recommended topics need to be prepared for more advance courses, say on verification, etc. In the area of research, a vigorous promotion of the conference Logic In Computer Science (LICS) and the journal TOCL will be undertaken. Actions towards increasing the quality of publications (such as awards to best papers) and participation will also be identified and promoted. Also, incorporation of special sessions in other related leading CS conferences (e.g., CAV) will be investigated and appropriate action will be undertaken. I will also strive towards closer collaboration of conferences and journals closely related to Logic in Computer Science and Formal Methods. Furthermore, I will also promote special issues of top journals on Logic in CS. Closer collaboration with EATCS an EACSL will be sought. I will also undertake promotion of diversity and encouragement of young researchers.

Candidate for Vice-Chair

Véronique Cortier
CNRS, Lorraine University, Nancy, France

BIOGRAPHY

Academic Background:

Ph.D., ENS Cachan School, 2003, Computer Science.

Professional Experience:

Research Director, CNRS, Lorraine University, Nancy, France, 2010 – Present;
Researcher, CNRS, Lorraine University, Nancy, France, 2003 – 2010.

Professional Interest:

Verification; Security; Protocols; Voting.

ACM Activities:

SIGLOG Executive Committee Member, SIGLOG, 2014 – Present;
TISSEC Editorial Member, 2015 – Present.

Membership and Offices in Related Organizations:

CSF Steering Committee, IEEE, 2013 – Present;
POST Steering Committee, 2014 – Present;
CSF Program Committee Chair, IEEE, 2012 – 2013.

Awards Received:

Inria - French Académie des sciences Young Researcher Award, 2015;
IEEE Technical Committee on Security and Privacy, 2013;
Top Cited TCS Paper Award, 2010.

STATEMENT

SIGLOG is a young Special Interest Group while there is a large and active community working in logic and computation. I have been a member of the SIGLOG Executive Committee since its start in 2014 and I am pleased to be running for the Vice Chair position, to help SIGLOG to grow and gather the community in logic and computation.

SIGLOG has been very active since its birth with the launch of an enjoyable and instructive newsletter and the creation of the Church Prize that awards outstanding contributions in logic and computation. There is already a large community in logic as shown by the strong conferences in the field such as LICS. If elected, I will work together with the Executive Committee members to make SIGLOG a strong and unified community that gathers and structures the existing community in logic and computation.

Candidate for Vice-Chair

Martin Hofmann
University of Munich, LMU, Munich, Germany

BIOGRAPHY

Academic Background:

Ph.D., University of Edinburgh, 1995, Computer Science.

Professional Experience:

Full Professor, University of Munich, LMU, Munich, Germany, 2001 – Present;
Associate Professor, University of Darmstadt, TUD, Darmstadt, Germany, 2001;
Lecturer/Reader, University of Edinburgh, Edinburgh, Scotland, UK, 1998 – 2001.

Professional Interest:

Applications of logic and type theory to programming; Type-based resource analysis;
Limplicit computational complexity; Rigorous justification of program transformations;
Type systems for secure programming.

ACM Activities:

General Chair, POPL 2007, SIGPLAN, 2006 – 2007.

Awards Received:

IEEE-LICS Test of Time award, 2014;
Distinguished Dissertation Award of the British Computer Society, 1995.

STATEMENT

My research agenda is rather broad ranging from seminal works on dependent type theory (LICS 1995) to finite model theory (LICS 2009) and abstract interpretation (LICS-CSL 2014). The unifying theme has always been the application of logic, semantics, and types to concrete questions from programming and verification. Over and over, I find that the notions of logic, here including semantics and type theory, crop up and turn out useful in unexpected situations. I was therefore very pleased to see that SIGLOG has been formed and fully subscribe to its motto of "logic/informatics = calculus/physics&engineering". I felt honoured to be invited to serve as Vice-Chair and would be very glad to contribute in this way.

My particular strength w.r.t. to this office could be the breadth of my work allowing me to appreciate works from, e.g., linear logic and proof theory, as well as those from finite model theory leaning towards complexity, all the way to automatic verification. I see the role of SIGLOG as providing a platform for relating these and other often rather disjoint subfields of logic in computer science, and would like to help make that happen.

Candidate for Vice-Chair

Luke Ong
University of Oxford, Oxford, United Kingdom

BIOGRAPHY

Academic Background:

Ph.D., Imperial College London, 1988, Computer Science.

Professional Experience:

Professor of Computer Science, University of Oxford, Oxford, UK, 2004 – Present;
Director of Graduate Studies, Department of Computer Science, University of Oxford, UK,
2011 – 2015

Fellow, Merton College, Oxford, UK, 1994 – Present.

Professional Interest:

Semantics of computation; Verification; Programming Languages.

ACM Activities:

Vice Chair, ACM SIGLOG, 2014 – Present;

General Chair, ACM-IEEE Symposium on Logic in Computer Science, 2013 – 2015;

Steering Committee Member, ACM-IEEE Symposium on Logic in Computer Science,
2015 – 2018.

Membership and Offices in Related Organizations:

Steering Committee Chair, ACM-Incoop Conf. Formal Structures in Comp. & Deduction,
2015 – 2018;

Council Member, European Association of Theoretical Computer Science, 2012 – 2017;

Board Member, European Association of Computer Science Logic, 2007 – 2016.

STATEMENT

I would like to see SIGLOG develop into a natural and welcoming home for researchers and practitioners worldwide, interested in the connections between logic and computer science. On the theoretical side, SIGLOG embraces “Volume B” theoretical computer science, which has traditionally had a larger following in Europe than in North America. I aim to work to promote and raise the profile of logic in computation, not just in North America, but also in Asia and other continents. At the same time, I hope to make SIGLOG a more appealing professional body to our European colleagues. I believe it is very important that SIGLOG remains relevant to colleagues who apply logic in their work, but who may not consider themselves theoreticians. A model I like is the broad church

that is Federated Logic Conference, where there is cross fertilisation of ideas because people from a number of communities interact, using logic as a common language. SIGLOG can strengthen its community by instituting awards to recognise the achievements and contributions of its members; it can nurture its younger and students members by sponsoring their professional developments. These are two tasks I consider strategic and aim to champion.

Candidate for Vice-Chair

Leszek Pacholski
University of Wroclaw, Wroclaw, Poland

BIOGRAPHY

Academic Background:

dr hab (Polish Higher Doctorate), Polish Academy of Science, 1972, Mathematics.

Professional Experience:

Professor, Department Chair, University of Wroclaw, Department of Computer Science, Wroclaw, Poland, 1996 – 2015;

President, University of Wroclaw, Wroclaw, Poland, 2005 – 2008;

Chair of the Logic Group, Mathematical Institute of the Polish Academy of Science, Warsaw, Poland, 1973 – 1994.

Professional Interest:

Finite model theory; Model theory.

ACM Activities:

Member, Committee for the Best Dissertation Award, 2000 – 2005;

Member, Membership Activities Board, 1998 – 2001.

Membership and Offices in Related Organizations:

Member, LICS Advisory Board, 1995 – 2010;

Member, EACSL Executive Committee, 1992 – 1997;

Board Member, Informatics Europe, 2011 – Present.

STATEMENT

I am deeply honored by the nomination to run for the SIGLOG Vice-Chair. For over 45 years I have been a member of logic community – first in pure logic and then in logic and computation. For 9 years I have served as a member and then chair of committee for Logic in Europe of the Association for Symbolic Logic (ASL), responsible for organization of Logic Colloquium, the annual ASL meeting in Europe. I have served on the committee of the ACM dissertation award. For many years I have also been a member of the Advisory Committee of LICS. Since its beginning I have served on the editorial board of ACM Transactions on Computational Logic.

The SIGLOG community is very large and diverse. It consists of several sub-communities ranging from abstract finite model theory, through term rewriting, automated deduction, SAT solving to foundations of programming languages, data bases, security and others. One of the main problem of this community is keeping it together and preventing separation of subareas and isolation from the core and applied computer science. I believe that ACM TOCL and first of all LICS should serve as the vehicle to maintain unity of this community.

Candidate for Vice-Chair

Frank Pfenning
Carnegie Mellon University, Pittsburgh, PA USA

BIOGRAPHY

Academic Background:

Ph.D., Carnegie Mellon University, 1987, Mathematics.

Professional Experience:

Head, Department of Computer Science, Carnegie Mellon University, Pittsburgh, PA, 2013 – Present;

Professor, Department of Computer Science, Carnegie Mellon University, Pittsburgh, PA, 2002 – Present;

Associate Dean, School of Computer Science, Carnegie Mellon University, Pittsburgh, PA, 2009 – 2010.

Professional Interest:

Logic in Computer Science; Programming Languages; Automated Deduction; Computer Security; Computer Science Education.

ACM Activities:

Organizing Committee, LICS, ACM/IEEE Joint Conference, 2008 – 2010;

Program Chair, LICS, ACM/IEEE Joint Conference, 2002;

General Chair, PPDP, ACM SIGPLAN, 2002;

Program Chair, PPDP, ACM SIGPLAN, 2000.

Membership and Offices in Related Organizations:

President, CADE, Inc., 2003 – 2004;

Trustee, CADE, Inc., 1998 – 2004;

Steering Committee Member, ETAPS Conference, 2012 – 2013.

Awards Received:

ACM Fellow, 2015;

Alexander-von-Humboldt Fellowship, 1996;

Fulbright Scholarship, 1980.

STATEMENT

Logic and computation are connected in many different ways. To name just a few, there is the correspondence between proofs and programs, there are logics for reasoning about programs, and there is the central role of proof theory and model theory in automated

deduction and verification. Because of its strong influence in many areas of computer science it is sometimes neglected as a field of its own. I would like to further its independent development, specifically in undergraduate and graduate education, including curriculum development and organization of summer schools. I see creating awards, an effort that has been underway since the creating of SIGLOG, as another excellent way to strengthen the profile of the field. As an emerging special interest group, we should also reach out and build strong connections with organizations such as the European Association for Computer Science Logic (EACSL), the Association for Automated Reasoning (AAR), and the Association for Symbolic Logic (ASL).

Candidate for Secretary

Zakaria Chihani
CEA, Saclay-France

BIOGRAPHY

Academic Background:

Ph.D., École polytechnique, 2015, Computer Science.

Professional Experience:

Postdoctoral Fellow, CEA, Saclay-France, 2015 – Present.

Professional Interest:

Foundational Proof Theory; Proof Checking; Declarative Languages; Boolean Satisfiability; Constraint Satisfaction Problem.

STATEMENT

I feel grateful that my application for the position of Secretary for ACM SIGLOG is considered. As a new member, I cannot ascertain that I realize the sheer importance of the role of ACM in general and of SIGLOG in particular to the logic world today. But already what I know of its activities gives me pride in being a member of this community. Having experienced university both from the perspective of a student and, to a certain extent, that of a teacher, the promotion of logic to undergraduate students is particularly dear to me. It would, therefore, bring me great honor to take part in the work of this group. I am aware that my lack of experience does not play in my favor, but it is balanced by the deep respect for the task and the eagerness to learn that, if chosen, I would apply to my work.

Candidate for Secretary

Elaine Pimentel
Federal University of Rio Grande do Norte, Natal, Brazil

BIOGRAPHY

Academic Background:

Ph.D., Federal University of Minas Gerais, Brazil, 2001, Proof Theory.

Professional Experience:

Associate Professor, Federal University of Rio Grande do Norte, Natal, Brazil,
2014 – Present;

Associate Professor, Federal University of Minas Gerais, Belo Horizonte, Brazil,
1994 – 2013.

Professional Interest:

Proof Theory; Logical Frameworks; Mathematical logic; Model theory; Topology.

ACM Activities:

Professional Member, ACM, 2013 – Present.

STATEMENT

SIGLOG is relatively new, hence running for a position of secretary in this SIG is quite challenging and exciting. As a secretary, I plan to contribute to SIGLOG by acting in the following fronts:

1. increase membership in SIGLOG by suggesting:
 - a) alternative publicity programs; and
 - b) a broader number of conferences to be supported/sponsored by SIGLOG;
2. increase the international impact of SIGLOG, especially in emerging academic centers in South America;
3. help enhancing ACM scholarship programs, such as ACM-W, in order to foster the participation of woman and developing countries students in conferences in Logic and related areas.

Candidate for Secretary

Alexandra Silva
University College London, United Kingdom

BIOGRAPHY

Academic Background:

Ph.D., Radboud University Nijmegen, 2010, Computer Science.

Professional Experience:

Senior Lecturer, University College London, United Kingdom, 2015 – Present;
Assistant Professor, Radboud University Nijmegen, The Netherlands, 2011 – 2015;
Post-doc, Cornell University, Ithaca, NY, 2011.

Professional Interest:

Programming Languages; Coalgebra; Semantics; Verification; Formal Languages.

ACM Activities:

Secretary, SIGLOG, 2013 – Present.

Membership and Offices in Related Organizations:

Member Steering Committee, LICS ACM/IEEE, 2015 – Present.

STATEMENT

It is my great pleasure to run for the Secretary position of ACM SIGLOG. During the past decades there has been extensive, continuous, and growing interaction between logic and computer science. The logic and, more broadly, the formal methods community has grown in numbers and diversity. There are now many highly-attended conferences that testify to the vitality of the community, which had functioned without a unifying organisation until the creation of ACM SIGLOG two years ago.

If elected, I will:

- Advocate the importance of logic and formal methods in Computer Science Education.
- Devise strategies to create a fair atmosphere in SIGLOG sponsored events in order to foster the increase of women and minorities in the community.
- Stimulate student participation in conferences through travel grants.

I am committed to strengthening the leadership role of ACM SIGLOG in the logic, computation, and formal methods community.

Candidate for Treasurer

Amy Felty
University of Ottawa, Ontario, Canada

BIOGRAPHY

Academic Background:

Ph.D., University of Pennsylvania, 1989, Computer Science.

Professional Experience:

Professor, University of Ottawa, Ontario, Canada, 2000 – Present;
Research Scientist, Bell Labs, Murray, Hill, NJ, 1991 – 1999;
Visiting Researcher, Inria, Sophia Antipolis, France, 1989 – 1991.

Professional Interest:

Theorem proving; Program verification; Formal methods; Logical frameworks;
Trustworthy computing.

ACM Activities:

PC Member, ACM/IEEE Symposium on Logic in Computer Science, 2016;
PC Member, ACM Symposium on Applied Computing, SVT Track, 2009 – 2012;
PC Member, ACM Workshop on Formal Methods in Security Engineering, – 2007.

Membership and Offices in Related Organizations:

Steering Committee Member, International Conference on Interactive Theorem Proving, 2012 – Present;
Steering Committee Member, International Workshop on Logical Frameworks and Meta-languages, 2009 – Present;
Trustee (Steering Committee Member), International Conference on Automated Deduction, 2004 – 2015.

STATEMENT

I am honored to have the opportunity to run for Treasurer of SIGLOG. I have been a continuous ACM member since 1992, and before that was a member for many years as an undergraduate and graduate student. I have been a member of SIGLOG since its inception almost two years ago.

I have a broad range of professional experience that I believe will serve me well as a member of the SIGLOG executive committee. This experience includes serving as PC chair, steering committee member, and PC member of numerous conferences in logical foundations of computer science, formal methods, and programming languages.

As treasurer, I would welcome the opportunity to support the growth and success of SIGLOG. I strongly support the central goals of the organization as outlined in the founding documents, including advocating for the importance of logic in the undergraduate computer science curriculum and establishing prizes that recognize the significant contributions of members of our community.

Candidate for Treasurer

Vivek Nigam

Federal University of Paraíba UFPB, João Pessoa, Brazil

BIOGRAPHY

Academic Background:

Ph.D., Ecole Polytechnique, 2009, Computational Logic.

Professional Experience:

Associate Professor, Federal University of Paraíba (UFPB), João Pessoa, Brazil, 2012 – Present;

Postdoctoral Researcher, Ludwig Maximilian University of Munich (LMU), Munich, Germany, 2010 – 2012;

Postdoctoral Researcher, University of Pennsylvania, Philadelphia, PA, 2009 – 2010.

Professional Interest:

Computational Logic; Formal Methods; Foundations of Computer Security; Proof Theory; Logical Frameworks.

STATEMENT

I am honoured to be considered for the position as SIGLOG's Treasurer. I fondly remember my first ACM venue at PPDP'09. Since then I participated with articles as well as PC-membership in many ACM (LICS, FSCD, POPL, PPDP) and other logic-related venues (CSL, RTA/TLCA, CONCUR, WOLLIC, among others). My work is tightly aligned with SIGLOG's overall goal of advancement of logic and computation, formal methods in Computer Science, with works in Logical Frameworks, Computer Security, Formal Methods, and Programming Languages. I am an active member of the Brazilian logic community being this year's LSFA co-chair, the main Brazilian logic forum, to be held in conjunction with FSCD, and member of LSFA's Steering Committee. I also co-organised in 2015 the Shonan Meeting of Logic and Verification Methods in Security and Privacy bringing together leading researchers to discuss relevant topics in the field.

As SIGLOG's Treasurer, I will work closely with the SIGLOG officers making sure that SIGLOG's finances run smoothly and transparently preparing clear and comprehensive financial reports. I will take an active interest in the forthcoming initiations of SIGLOG helping foster SIGLOG's visibility, particularly in South America.

Candidate for Treasurer

Ramaswamy Ramanujam
Institute of Mathematical Sciences, Chennai, India

BIOGRAPHY

Academic Background:

Ph.D., Tata Institute of Fundamental Research, Mumbai, 1988, Computer Science.

Professional Experience:

Faculty, Institute of Mathematical Sciences, Chennai, India, 1988 – 2016.

Professional Interest:

Mathematical Logic; Automata theory; Game theory; Proof complexity; Security theory.

ACM Activities:

Area Editor, ACM Transactions on Computational Logic, 2011 – 2016.

Membership and Offices in Related Organizations:

Convener, Executive Council, Association for Logic in India, 2007 – 2011;
Member, Executive Council, Association for Symbolic Logic, 2011 – 2013;
Member, Executive Council, Indian Association for Research in Computing Science, 2002 – 2008.

STATEMENT

I feel honoured to have been asked to run for office in the ACM SIGLOG election. I have been a member of the ACM for some years and have been active in ACM India as well. As Area Editor of ACM ToCL, I have been associated with one of the central activities of the Logic subcommunity within computer science. This is a community that has the potential to expand and grow significantly in Asia, especially in south Asia and East Asia where it is currently rather miniscule. My work in the Indian Association for Research in Computing Science, the Association for Logic in India and in the Council of the Association for Symbolic Logic over the last two decades convinces me that Logic and Computing Science have a great deal to contribute to each other not only as disciplines but also in our professional development. I hope that we can bring a better understanding of diverse educational contexts to develop our community as a whole, the world over.

Candidate for Treasurer

Natarajan Shankar
SRI International, Menlo Park, CA, USA

BIOGRAPHY

Academic Background:

Ph.D., The University of Texas at Austin, 1986, Computer Science.

Professional Experience:

Principal Scientist, SRI International, Menlo Park, CA, 2014 – Present;
Staff Scientist, SRI International, Menlo Park, CA, 1998 – 2014;
Senior Computer Scientist, SRI International, Menlo Park, CA, 1994 – 1998.

Professional Interest:

Logic; Formal Methods; Automated Theorem Proving; Computer-Aided Verification;
Cyber-physical systems

ACM Activities:

Treasurer, SIGLOG, 2014 – Present.

Awards Received:

CAV Award, 2012.

STATEMENT

Logic is the calculus of computing and it is a privilege to be part of the creation and organization of SIGLOG. The key challenge for SIGLOG is in serving and representing the interests of the hundreds of researchers, students, and professionals working on logic-related topics. As the organizer of a summer school series and in my association with various conferences, I have been able to reach out to young researchers to convey the benefits of being part of SIGLOG. Expanding enrollment will allow SIGLOG to institute prestigious awards and sponsor exciting conferences. As computing evolves and confronts challenges of immense complexity, logic will play an increasingly critical role in building a strong intellectual foundation. It is my hope that over the next few years, SIGLOG will become the premier international forum for representing the expanding range of activities at the intersection of logic and computing.