

FCRC 2007

June 8 - 16, San Diego, CA

TIMELINE SCHEDULE

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CONFERENCE/WORKSHOP/EVENT ACRONYMS & DATES

Dates	Full Name	Dates	Full Name		
ASGI	10	Workshop on Architectural Support for Giga-Scale Integration (ISCA)	NSC	9	Workshop on Non-Silicon Computation (ISCA)
CCC	11--15	CRA Computing Community Consortium @ FCRC	PADS	12--15	Workshop on Principles of Advanced and Distributed Simulation (ACM SIGSIM, IEEE-TCSIM, & SCS)
COLT	13--15	Conference on Learning Theory	PASTE	13--14	ACM SIGPLAN-SIGSOFT Workshop on Program Analysis of Software Tools and Engineering
Complex	13--16	IEEE Conference on Computational Complexity	PLAS	14	Programming Languages and Analysis for Security Workshop
CRA-W	9--10	CRA-W Career Mentoring Workshop	PLDI	10--13	ACM SIGPLAN Conference on Programming Language Design and Implementation
DEECS	12	International Workshop on Data Engineering Issues in E-Commerce and Services (ECOM)	SPAA	9--11	ACM Annual Symposium on Parallelism in Algorithms and Architectures (ACM SIGACT & SIGARCH)
ECOM	11--15	ACM Conference on Electronic Commerce	SSRE	12	Session on Research Ethics
ExpCS	13--14	Workshop on Experimental Computer Science	SSW	13	SIGMETRICS Student Workshop (METRICS)
HOPL	9--10	ACM-SIGPLAN History of Programming Languages Conference	STOC	11--13	ACM Symposium on Theory of Computing
ISCA	9--13	International Symposium on Computer Architecture (ACM SIGARCH & IEEE-TCCA)	VEE	13--15	International Conference on Virtual Execution Environments
LCTES	13--15	Languages, Compilers, and Tools for Embedded Systems (ACM SIGPLAN)	WARP	9	Workshop on Architectural Research Prototyping (ISCA)
MAMA	13	Workshop on Mathematical Performance Modeling and Analysis (METRICS)	WCAE	9	Workshop on Computer Architecture Education (ISCA)
METRICS	12--16	ACM SIGMETRICS International Conference on Measurement and Modeling of Computer Systems	WDDD	10	Workshop on Duplicating, Deconstructing, and Debunking (ISCA)
MineNet	12	Workshop on workshop on Mining Network Data (METRICS)	WIOSCA	10	Workshop on Interaction between OS and Architecture (ISCA)
MoBS	10	Workshop on Modeling, Benchmarking, and Simulation (ISCA)	WPM	12	Workshop on Prediction Markets (ECOM)
NE-IBC	11	Workshop on Economics of Networked Systems/Incentive-Based Computing (ECOM)			

For affiliated workshops, the main event to which they are affiliated is given in parentheses. Dates include affiliated tutorials, but not affiliated workshops.

SPECIAL EVENTS not on Timeline Schedules

(NOTE: Conference-specific special events are typically open only to those who have registered for the conference in question, and the ACM Awards Banquet is for ticket holders only)

Friday, June 8	Saturday, June 9	Sunday, June 10	Monday, June 11	Tuesday, June 12	Wednesday, June 13	Thursday, June 14	Friday, June 15
<p>SPAA Reception 7:00 - 9:00 pm Tiki Pavillion</p>	<p>ACM Awards /Banquet Hotel del Coronado, Coronado Island 6:00 - 10:00 pm</p> <p>CRA-W Banquet 6:30 - 8:30 pm Tiki Pavilion</p>	<p>ACM Turing Award Lecture Fran Allen 5:45 - 7:00 pm Grand Exhibit Hall</p> <p>ISCA Reception 7:30 - 9:30 pm Golden Ballroom</p> <p>PLDI Reception + Student Posters 7:00 - 9:00 pm Town & Country</p> <p>STOC Reception 7:00 - 9:00 pm Sunrise/Sunset</p> <p>SPAA Banquet/Business Meeting 7:00 - 10:00 pm Pacific 3</p>	<p>CCC Christos Papadimitriou <i>The Algorithmic Lens: How the Sciences are Being Transformed by the Computational Perspective</i> 6:00 - 7:00pm Grand Exhibit Hall</p> <p>PLDI Awards Luncheon 12:30 - 1:30 pm Grand Exhibit Hall</p> <p>ISCA Banquet at San Diego Zoo (Sponsored by Intel) Buses Leave at 6:00 pm</p> <p>PLDI Future Directions Report 6:00 -6:30 pm Town & Country</p> <p>PLDI Memorial: Backus & Kennedy 6:30 -7:00 pm Town & Country</p> <p>STOC Student Poster Session 8:30 - 9:00 pm Golden Ballroom</p> <p>STOC Business Meeting 9:00 -10:30 pm Golden Ballroom</p>	<p>CCC Bob Colwell <i>Future of Computer Architecture '07</i> 6:00 - 7:00pm Grand Exhibit Hall</p> <p>SSRE Panel: Research Ethics 5:50 - 7:00 pm Pacific Salon 3</p> <p>ISCA Awards Luncheon 12:30 - 2:00 pm Grand Exhibit Hall</p> <p>METRICS Student Lunch + Lecture 12:30 - 1:30 pm Pacific Salon 6 & 7</p> <p>ISCA SIGARCH/TCCA Business Mtg 7:00 - 8:30 Golden West / California</p> <p>PADS Reception 5:00 - 7:00 pm Tiki Pavillion</p> <p>COLT Reception 6:00 - 9:00 pm Sunrise</p> <p>Complex Reception 8:00 - 10:00 Windsor</p> <p>PLDI Student Poster Presentations 8:00 - 10:00 Town & Country</p>	<p>CCC Randall Bryant <i>Data-Intensive Super Computing: Taking Google-Style Computing Beyond Web Search</i> 6:00 - 7:00pm Town & Country Ballroom</p> <p>Complex Rump Session 6:00 - 7:30 pm Royal Palm 1-4</p> <p>ECOM Business Meeting 6:00 - 7:00 pm Sunrise</p> <p>VEE Reception and Dinner 6:00 - 8:00 pm Trellises Poolside</p> <p>METRICS Reception 6:30-8:30 pm Tiki Pavilion</p> <p>PADS Business Meeting 5:30 - 6:00 pm Sunset</p> <p>COLT Business Meeting 8:00 - 9:30 pm Pacific Salon 3</p> <p>COLT Open Problem Session 9:30 - 10:30 pm Pacific Salon 3</p>	<p>CCC Scott Shenker <i>We Dream of GENI: Exploring Radical Network Designs</i> 6:00 - 7:00pm Grand Exhibit Hall</p> <p>ECOM Reception and Banquet 6:00 - 9:00 pm Rose Garden & Sunrise</p> <p>LCTES Banquet & Keynote 6:30pm San Diego</p> <p>PADS Banquet 7:00 - 9:00 pm El Agave Restaurant</p> <p>COLT Banquet 7:00 - 9:00 pm Pacific 1 & 2</p> <p>COLT Impromptu Session 9:00 - 10:30 pm Pacific Salon 3</p> <p>Complex Buseness Meeting 9:00 - 10:00 pm Royal Palm 1-4</p>	<p>METRICS Banquet & Keynote 6:30pm Sunrise/Sunset</p>

Continental Breakfast 8:00 - 9:00 am, Sat 6/9 - Sat 6/16 Atlas Foyer
Morning Coffee Break 9:45 - 10:30 am, Sat 6/9 - Sat 6/16 Atlas Foyer

Lunch 12:30 - 1:30 pm, Sat 6/9 - Fri 6/15 Lion Fountain Court
Afternoon Coffee Break 3:30 - 4:00 pm, Sat 6/9 - Fri 6/15 Atlas Foyer

Saturday Morning, June 9

	8:30	8:40	8:50	9:00	9:10	9:20	9:30	9:40	9:50	10:00	10:10	10:20	10:30	10:40	10:50	11:00	11:10	11:20	11:30	11:40	11:50	12:00	12:10	12:20						
*CRA-W	Welcome		Keynote Address: Robin Murphy				Networking and Professional Interaction Jan Cuny						COFFEE BREAK			Panel: Securing Funding for Research														
HOPL			Introduction		Keynote: Guy Steele and Richard Gabriel						COFFEE BREAK			AppleScript William Cook				The Evolution of Lua Roberto Ierusalimschy, Luiz Henrique de Figueiredo, & Weldemar Celes												
ISCA	Tutorial: Inference and Learning for Large Scale Microarchitectural Analysis																													
NSC			Efficient storage of defect maps for nanoscale memory <small>Biswas, Chong, Kastner, Sherwood & Metodi</small>				Image processing with CMOL circuits <small>Dmitri Strukov & Konstantin Likharev</small>				COFFEE BREAK			Exploiting detachability: A non-silicon approach to polymorphism <small>Hashem Najaf-Abadi & Eric Rotenberg</small>			Nano-enhanced architectures: Using carbon nanotube interconnects in cache design <small>Agrawal, Srivastava, Chong, Banerjee, et al.</small>			Energy transfer logic on DNA nanostructures: Enabling molecular-scale amorphous computing <small>Dwyer, Lebeck, & Pistol</small>			Probabilistic analysis of a quantum-dot cellular automata multiplier implemented in different technologies <small>Timothy Dysart & Peter Kogge</small>							
SPAA			Universal augmentation schemes for network navigability: Overcoming the sqrt(n)-barrier <small>Fraigniaud, Gavaille, Kosowski, et al.</small>			On triangulation of simple networks <small>Robert Krauthgamer</small>			Strongly-bounded sparse decompositions of minor free graphs <small>Abraham, Gavaille, Makhi, & Wieder</small>			COFFEE BREAK			Approx. algs for multiprocessor scheduling under uncertainty <small>Guolong Lin & Rajmohan Rajaraman</small>			Scheduling DAGs on asynchronous processors <small>Michael Bender & Cynthia Phillips</small>			Speed scaling on parallel processors <small>Albers, Muller, & Schmelzer</small>			Brief Announcements I: Parallel & Multicore Systems						
WCAE			WELCOME		Keynote: Yale Patt LC-3, x86, or MIPS: The first ISA for Students to Study						COFFEE BREAK			A pedagogically targeted logic design and simulation tool <small>David Poplawski</small>			VLIW-DLX simulator for educational purposes <small>Milos Becvar & Stansislav KahaneK</small>			From classroom to research: providing different services for computer arch. educ. <small>Soares & Wagner</small>			Animations of important concepts in parallel computer architecture <small>Gambhir, Gehringer, et al.</small>			Understanding cache hierarchy interactions with a program-driven simulator <small>Pascual, Torrenti, et al.</small>			Discussion	

* Open only to those who registered for this event

ROOMS

CRA-W:	Sunrise	NSC:	Terrace 3
HOPL:	Pacific 1 & 2	SPAA:	Pacific 3
ISCA Tutorial:	Royal Palm 3 & 4	WCAE:	Royal Palm 1 & 2

Saturday Afternoon, June 9

	1:30	1:40	1:50	2:00	2:10	2:20	2:30	2:40	2:50	3:00	3:10	3:20	3:30	3:40	3:50	4:00	4:10	4:20	4:30	4:40	4:50	5:00	5:10	5:20	5:30	5:40	5:50			
*CRA-W				Time Management, Family, and Quality of Life Issues Kathleen Fisher & Chandra Krintz									COFFEE BREAK			Panel: <i>The Job Search Process</i>														
HOPL	A History of Modula-2 and Oberon Nklaus Wirth				Evolving a Language in and for the Real World: <i>C++ 1991-2006:</i> Bjarne Stroustrup				COFFEE BREAK			Statecharts in the Making: A Personal Account David Harel				A History of Erlang Joe Armstrong														
ISCA	Tutorial: Inference and Learning for Large Scale Microarchitectural Analysis																													
NSC	A combinatorial noise model for quantum computer simulation Chi, Lyon, & Martonosi			Hardware system considerations for trapped ion quantum information processors Jungsang Kim & Changsoo Kim			Preliminary analysis of the scalability of ion-trap devices for large-scale quantum computation Metodi, Thaker, & Chong			CMOL technology development roadmap Konstantin Likharev & Dmitri Strukov			COFFEE BREAK			Panel Discussion														
SPAA			Optimal sparse matrix dense vector multiplication in the I/O model Vicari, Jacob, Bender, Brodal, et al.		The cache-oblivious Gaussian elimination paradigm Chowdhury & Ramachandran		Cache-oblivious streaming B-trees Bender, Farach-Colton, Fineman, et al.		An experimental comparison of cache-oblivious and cache-aware programs Yotov, Roeder, Pingali, Gunneis, et al.		COFFEE BREAK			Scheduling threads for constructive cache sharing on CMPs Chen, Gibbons, Kozuch, Laskovitis, et al.		SuperMatrix out-of-order scheduling of matrix operations for SMP and multi-core architectures Chan, Quintana-Orti, et al.		Proximity-aware directory-based coherence for multi-core processor architectures Brown, Kumar, & Tullsen		A parallel dynamic programming algorithm on a multi-core architecture Tan, Sun, & Gao										
WARP	Prototyping the TRIPS scalable distributed processing system Stephen Keckler & Doug Burger			Prototyping the scale vector-thread processor Krashinsky, Batten, & Asanovic			Building various levels of SOC architecture exploration environments: system level simulator, emulator, & ... Park, Oh, Kwak, Kyung, Im, Cho, et al.			RiceNIC: prototyping network interfaces Jeffrey Shaler & Scott Fixner			COFFEE BREAK			The performance of FPGA performance models Michael Pellauer & Joel Emer		Resource-efficient FPGA content-addressable memories Kermin Fleming & Joel Emer		Virtualizing Emulation Chung, Nurvitadhi, Hoe, Falsafi, & Mai		An infrastructure for HW/SW partitioning & synthesis of architectural simulators Penry, Ruan, & Rehme								
WCAE	FEATURED TALK An integrated approach to teaching computer systems architecture Umakishore Ramachandran & William Leahy			An experimental laboratory environment for teaching embedded hardware systems Dennis Brylow		Introducing power & energy notions in computer architecture laboratories Perez, Su'ez, & Yufera		CHIPDESIGN: From theory to the real world Papa-Vaya, Jambor, et al.		Discussion		COFFEE BREAK			SPECIAL SESSION Industry perspective on chip multi-threading: Bridging the gap with academia using OpenSPARC Dwayne Lee, et al.					Pedagogic value .. Of implementing Marie computer from Null & Lobur... Stanley, Embrey, et al.		Teaching by building a computer Umess	Discussion							

* Open only to those who registered for this event

ROOMS

CRA-W:	Sunrise	NSC:	Terrace 3
HOPL:	Pacific 1 & 2	SPAA:	Pacific 3
ISCA Tutorial:	Royal Palm 3 & 4	WARP:	Terrace 1 & 2
		WCAE:	Royal Palm 1 & 2

Sunday Morning, June 10

	8:00	8:15	8:30	8:45	9:00	9:10	9:20	9:30	9:40	9:50	10:00	10:10	10:20	10:30	10:40	10:50	11:00	11:10	11:20	11:30	11:40	11:50	12:00	12:10	12:20
ASGI				WELCOME	A model for parameter variation and resulting timing errors for micro-architects <small>Teodorescu, Greskamp, Nakano, et al.</small>	Mitigating the effects of proces variations: Architectural approaches for improving batch performance <small>Das, Ozdemir, Memik, Zambreno, & Choudhary</small>			Process variation tolerant register files based on dynamic memories <small>Liang, Canal, Wei, & Brooks</small>			COFFEE BREAK	Fault injection verification of IBM POWER6 soft error resilience <small>Kudva, Kellington, Sanda, McBeth, et al.</small>		Mitigating multi-bit soft errors in L1 caches using last-store prediction <small>Gold, Ferdman, Falsafi, & Mai</small>		Enabling on-chip switching regulators for multi-core processors using current staggering <small>Kim, Gupta, Wei, & Brooks</small>		WRAP-UP						
*CRA-W				Panel: <i>Building Your Research Career</i>									COFFEE BREAK		Panel A: <i>The Tenure Process</i>										
															Panel B: <i>Research Careers in Industry / National</i>										
HOPL	Intro	<i>The Rise & Fall of High Performance Fortran</i> <small>K. Kennedy, C. Koelbel, & H. Zima</small>			<i>The Design and Development of ZPL</i> <small>Lawrence Snyder</small>			COFFEE BREAK			<i>Self</i> <small>David Ungar & Randall Smith</small>			<i>The When, Why and Why Not of BETA</i> <small>Bent Kristensen, Ole Madsen & Birger Moller-Pedersen</small>											
*ISCA				Tutorial: <i>RAMP (Research Accelerator for Multiple Processors)</i>																					
MoBS				Keynote -- Pradip Bose <i>Raising the Level of Model Abstraction: Benefits, Limits, & Pitfalls</i>			COFFEE BREAK			AWB: The Asim architect's workbench <small>Joel Emer</small>		ATMI: Analytical model of temperature in microprocessors <small>Pierre Michaud & Yiannakis Sazeides</small>		ReTrace: Collecting execution trace with virtual machine deterministic replay <small>Xu, Malyugin, Sheldon, Venkitachalam, et al.</small>		Software infrastructure and tools for the TRIPS prototype <small>Yoder, Burrill, McDonald, Bush, Coons, et al.</small>									
PLDI				Tutorial 1: <i>Using FindBugs for Research</i>																					
				Tutorial 2: <i>X10: Concurrent Programming for Modern Architectures</i>																					
SPAA				Tight bounds for distributed selection <small>Kuhn, Locher, & Wattenhofer</small>	Local MST computation with short advice <small>Fraigniaud, Korman, & Lebar</small>		Distributed approximation of capacitated dominating sets <small>Kuhn & Moscibroda</small>			COFFEE BREAK			Packing to angles and sectors <small>Berman, Jeong, et al.</small>		Conflict-free coloring of rectangle ranges using $O(n^{3.82+\epsilon})$ colors <small>Ajwani, Elbassioni, et al.</small>		Balanced allocations with heterogeneous bins <small>Udi Wieder</small>		Brief Announcements II <i>Diverse Algorithms</i>						
WIOSCA				Keynote I -- Burton Smith <i>Many-core Operating System Challenges & Opportunities</i>			COFFEE BREAK			Desktop workload characterization for CMT/SMT and implications for operating system design <small>Bachthaler, Belli, & Fedorova</small>		The cost of IPC: An architectural analysis <small>Gelado, Cabezas, Vilanova, & Navarro</small>		Re-architecting VMMs for multicore systems: The sidecore approach <small>Kumar, Raj, Schwan, & Ganey</small>											

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ROOMS

ASGI:	Royal Palm 3 & 4	HOPL:	Pacific 1 & 2	PLDI Tutorial 1:	Pacific 4 & 5
CRA-W:	Sunrise	ISCA:	California	PLDI Tutorial 2:	Pacific 6 & 7
CRA-W Panel A:	Sunset	MoBS:	Terrace 1 & 2	SPAA:	Pacific 3
CRA-W Panel B:	Towne	PLDI Tutorial 2:	Pacific 6 & 7	WIOSCA:	Royal Palm 1 & 2

Sunday Afternoon, June 10

	1:30	1:40	1:50	2:00	2:10	2:20	2:30	2:40	2:50	3:00	3:10	3:20	3:30	3:40	3:50	4:00	4:10	4:20	4:30	4:40	4:50	5:00	5:10	5:20	5:30	5:45 - 7:00		
*CRA-W				Panel: Career Paths Contrasted						COFFEE BREAK			Panel A: Teaching, Mentoring, Service						Wrap Up and Evaluation									
Panel B: Strategies for Success in Industry and National Labs																												
HOPL	<i>The Development of the Emerald Programming Language</i> A. Black, N. C. Hutchinson, E. Jul, & H. M. Levy						<i>A History of Haskell: Being Lazy with Class</i> Paul Hudak, John Hughes, Simon Peyton Jones, & Philip Wadler						COFFEE BREAK			Panel: Programming Language Paradigms: Past, Present, and Future												
*ISCA	*Tutorial: RAMP (Research Accelerator for Multiple Processors) (Continued)																											
MoBS	High speed CPU simulation using JIT binary translation Nigel Topham			CoGS-Sim: Co-phase guided small-sample simulation of multi-threaded & multicore architectures Joshua Kihm & Daniel Connors			Panel: Future Challenges in Modeling, Benchmarking and Simulation																					
*PLDI	*Tutorial 3: Hands on Phoenix -- A Framework for Program Analysis and Transformation																											
*Tutorial 4: Using Pin for Compiler and Computer Architecture Research and Education																												
SPAA				The notion of a timed register & its application to indulgent synchronization Raynal & Taubenfeld			Nonblocking transactions without indirection using alert-on-updates Spear, Shriraman, Dalessandro, et al.			Time-based transactional memory with scalable time bases Riegel, Fetzer, & Felber			Deadlock-free scheduling of X10 computations with bounded resources Agarwal, Barik, Bonachea, et al.			COFFEE BREAK			Online frequency allocation in cellular networks Chan, Chin, Ye, & Zhang			Energy efficient randomized communication in unknown adhoc networks Berenbrink, Cooper, & Hu			Local strategies for maintaining a chain of relay stations between an explorer and a base station Dyria, Kutylowski, et al.			
WDDD	INTRO	Dependence-based scheduling revisited: A tale of two baselines Pierre Salverda & Craig Zilles			An improved block-based thermal model in HotSpot 4.0 with granularity considerations Huang, Sankaranarayanan, Ribando, Stan, & Skadron			Duplicating & verifying LogTM with OS support in the M5 simulator Geoffrey Blake & Trevor Mudge			COFFEE BREAK			Cache replacement policy revisited Mohamed Zahran			Panel: Common Pitfalls of Academic Research:						Conclusions & Feedback					
WIOSCA		Keynote II -- Beng-Hong Lim <i>Virtualizing Beyond the Box: Architectural Support for a Distributed, Virtualized Compute Fabric</i>						Managing shared L2 caches on multicore systems in software Tam, Azimi, Soares, & Stumm			Base vectors: A potential technique for microarchitectural classification of applications Dan Doucette & Alexandra Fedorova			COFFEE BREAK			Panel: Challenges and Opportunities for System Software in the Multicore Era											

Turing Award Lecture

Fran Allen

Grand Exhibit Hall

* Open only to those who registered for this event

ROOMS

CRA-W: Sunrise	ISCA: California	SPAA: Pacific 3
CRA-W Panel A: Sunset	MoBS: Terrace 1 & 2	WDDD: Royal Palm 3 & 4
CRA-W Panel B: Towne	PLDI Tutorial 3: Pacific 4 & 5	WIOSCA: Royal Palm 1 & 2
HOPL: Pacific 1 & 2	PLDI Tutorial 4: Pacific 6 & 7	

Monday Morning, June 11

	8:15	8:20	8:30	8:40	8:50	9:00	9:10	9:20	9:30	9:40	9:50	10:00	10:10	10:20	10:30	10:40	10:50	11:00	11:10	11:20	11:30 - 12:30															
*ECOM	*Tutorial 1: Prediction Markets: Economics, Computation, and Mechanism Design																																			
ISCA	Opening Remarks			Anton: A special purpose machine for molecular dynamics research <small>Shaw, Deneroff, Dror, Kuskin, Larson, et al.</small>				Power provisioning for a warehouse-sized computer <small>Fan, Weber, & Barroso</small>				COFFEE BREAK			Making the fast case common and the uncommon case simple in unbounded transactional memory <small>Blundell, Devietti, Lewis, & Martin</small>				Synchronization state buffer: Supporting efficient fine-grain synchronization on many-core architectures <small>Zhu, Sreedhar, Hu, & Gao</small>																	
				Virtual hierarchies to support server consolidation <small>Michael Marty and Mark Hill</small>				Virtual private caches <small>Nesbitt, Laudon, & Smith</small>																												
NE-IBC	Opening Remarks			An economically principled generative model for AS graph connectivity <small>Corbo, Jain, Mitzenmacher & Parkes</small>				Economics of overlay networks: An industrial organization perspective on network economics <small>Peyman Faratin</small>				A pluralist approach to interdomain communication security <small>Arvamopoulos & Rexford</small>			COFFEE BREAK			On the impact of P2P incentive mechanisms on user behavior <small>Zghaibeh & Anagnostakis</small>				BitStore: An incentive-compatible solution for blocked downloads in BitTorrent <small>Ramachandran, Sarma & Fearster</small>														
				Exterminator: Automatically correcting memory errors with high probability <small>Novark, Berger, & Zorn</small>				CheckFence: Checking consistency of concurrent data types on relaxed memory models <small>Burckhardt, Alur, & Martin</small>				Automatically classifying benign and harmful data races using replay analysis <small>Narayanaswamy, Wang, et al.</small>				Sound and precise analysis of web applications for injection vulnerabilities <small>Wasserman & Su</small>				COFFEE BREAK			Fault-tolerant typed assembly language <small>Perry, Mackey, Reis, et al.</small>				A certified type-preserving compiler from lambda calculus to assembly language <small>Adam Chlipala</small>				Certified self-modifying code <small>Cai, Shao, & Vaynberg</small>				Enforcing isolation and ordering in STM <small>Shpeisman, Menon, et al.</small>	
SPAA	Opening Remarks			Maximizing total upload in latency-sensitive P2P applications <small>Douceur, Lorch, & Moscibroda</small>				Bi-objective scheduling algorithms for optimiz. makespan & reliability on heterogeneous systems <small>Dongarra, Jeannot, Saule, & Shi</small>				Scheduling to minimize gaps and power consumption <small>Demaine, Ghodsi, Hajiaghayi,</small>				COFFEE BREAK			Brief Announcements III																	
				Statistically-hiding commitment from any one-way function <small>Iftach Haitner & Omer Reingold</small>				On achieving the "best of both worlds" in secure multiparty computation <small>Jonathan Katz</small>				Zero-knowledge from secure multiparty computation <small>Ishai, Kushilevitz, Ostrovsky, & Sahai</small>				COFFEE BREAK			Faster integer multiplication <small>Martin Furer</small>				Fourier meets Mobius: Fast subset convolution <small>Bjrklund, Husfeldt, Kaski, & Koivisto</small>													
STOC	Opening Remarks			Voronoi diagrams in $n \cdot \exp(O(\sqrt{\lg n}))$ time <small>Timothy Chan & Mihai Patrascu</small>				Lower bounds for 2-dimensional range counting <small>Mihai Patrascu</small>				Combinatorial complexity in O-minimal geometry <small>Saugata Basu</small>				COFFEE BREAK			Smooth sensitivity and sampling in private data analysis <small>Nissim, Raskhodnikova, & Smith</small>				The price of privacy and the limits of LP decoding <small>Dwork, McSherry, & Talwar</small>													

PLENARY TALK
Grand Exhibit Hall

A Framework for Innovation

Charles Moore, AMD

* Open only to those who registered for this event

ROOMS

ECOM Tutorial: Royal Palm 1 & 2
ISCA A: Golden West & California
ISCA B: San Diego
NE-IBC: Royal Palm 3 & 4

PLDI: Town & Country
SPAA: Pacific 3
STOC Session A: Golden Ballroom
STOC Session B: Pacific Salons 1 & 2

Monday Afternoon, June 11

	1:30	1:40	1:50	2:00	2:10	2:20	2:30	2:40	2:50	3:00	3:10	3:20	3:30	3:40	3:50	4:00	4:10	4:20	4:30	4:40	4:50	5:00	5:10	5:20	5:30	5:40	5:50									
*ECOM	*Tutorial 2: Online Trust and Reputation Systems																																			
ISCA	An effective hybrid transactional memory system with strong isolation guarantees <small>Minh, Trautmann, Chung, McDonald, et al.</small>			Performance pathologies in hardware transactional memory <small>Bobba, Moore, Volos, Yen, Hill, et al.</small>			MetaTM/TxLinux: Transactional memory for an operating system <small>Ramadan, Rossbach, Porter, et al.</small>			An integrated hardware-software approach to flexible transactional memory <small>Shriraman, Spear, Hossain, Marathe, et al.</small>			COFFEE BREAK			Carbon: Architectural support for fine-grained parallelism on chip multiprocessors <small>Kumar, Hughes, & Nguyen</small>			Hardware atomically for reliable software speculation <small>Neelakantam, Rajwar, Srinivas, et al.</small>																	
	Rotary router: An efficient architecture for CMP interconnection networks <small>Abad, Puente, Gregorio, & Prieto</small>			Flattened butterfly: A cost-efficient topology for high-radix networks <small>Kim, Dally, & Abts</small>			A novel dimensionally-decomposed router for on-chip communication in 3D architectures			Express virtual channels: Towards the ideal interconnection fabric <small>Kumar, Peh, Kundu, & Jha</small>						Core fusion: Accommodating software diversity in chip multiprocessors <small>Ipek, Kirman, Kirman, & Martinez</small>			Tailoring quantum architectures to implementation style: A quantum computer for mobile and persistent Qubits <small>Chi, Lyon, & Martonosi</small>																	
NE-IBC	Panel: Net Neutrality										COFFEE BREAK			Boycotting and extorting nodes in an internetwork <small>Levin, Bender, Lumezanu, et al.</small>			Automated design of near optimal auctions for real life <small>Rabinovich & Ronen</small>			Truthful polynomial time optimal welfare keyword auctions with budget constraints <small>Elan Pavlov</small>			BREAK	A decentralized auction framework to promote efficient resource allocation in open comput. grids <small>Laura Kang & David Parkes</small>			Efficient topologies in network routing games <small>Epstein, Feldman, & Mansour</small>									
PLDI	Valgrind: A framework for heavyweight dynamic binary instrumentation <small>Nethercote & Seward</small>			Improved error reporting for software that uses black box components <small>Ha, Rossbach, Roy, et al.</small>			Thin slicing <small>Sidharan, Fink, & Bodik</small>			Static specification inference using predicate mining <small>Ramanathan, Grama, et al.</small>			Automatic inference of optimizer flow functions from semantic meanings <small>Scherpelz, Lerner, et al.</small>			COFFEE BREAK			Automatic inversion generates divide-&-conquer parallel programs <small>Morita, Moriata, et al.</small>			EXOCHI: Arch. & program. environment for a multi-core / threaded system <small>Wang, Collins, China, et al.</small>			Sketching stencils <small>Solar-Lezama, Arnold, et al.</small>			Mace: Language support for building distributed systems <small>Killian, Anderson, et al.</small>			Combining events & threads for scalable network services... <small>Peng Li & Steve Zdancewic</small>			Reliable and efficient programming abstractions for wireless sensor nets <small>Kothari, Gummadi, et al.</small>		
SPAA	Rate vs. buffer size - Greedy information gathering on the line <small>Adi Rosen & Gabriel Scalosub</small>			Online collaborative filtering with nearly optimal dynamic regret <small>Baruch Awerbuch & Thomas Hayes</small>			Truthful unsplitable flow for large capacity networks <small>Azar, Gamzu, & Gutner</small>			On the convergence of multicast games in directed networks <small>Fanelli, Flammini, & Moscardelli</small>			COFFEE BREAK			Sparse parallel Delaunay mesh refinement <small>Hudson, Miller, & Phillips</small>			Using SIMD registers & instructions to enable instruction-level parallelism in sorting algorithms <small>Furtak, Amarel, & Niewiadomski</small>			A tight bound on online buffer management for two-port shared-memory switches <small>Kobayashi, Miyazaki, & Okabe</small>														
STOC	How to rank w. few errors: A PTAS for weighted feedback are set on tournaments <small>Kenyon-Mathieu & Schudy</small>			Approximation algorithms for budgeted learning problems <small>Sudipto Guha & Kamesh Murugala</small>			An approximation algorithm for max-min fair allocation of indivisible goods <small>Arash Asadpour & Amin Saberi</small>			Simple deterministic approximation algorithms for counting <small>Bayati, Gamarnik, Katz, Nair & Tetali</small>			COFFEE BREAK			Hardness of routing with congestion in directed graphs <small>Chuzhoy, Guruswami, Khanna, & Talwa</small>			Polynomial flow-cut gaps & hardness of directed cut problems <small>Julia Chuzhoy & Sanjeev Khanna</small>			Balanced max 2-sat might not be the hardest <small>Per Austrin</small>			A 3-query PCP over integers <small>Guruswami & Raghavendra</small>											
	On the submodularity of influence in social networks <small>Elchanan Mossel & Sebastien Roch</small>			First to market is not everything: An analysis of preferential attachment with fitness <small>Borgs, Chayes, Daskalakis, & Roch</small>			Stability of the max-weight routing & scheduling protocol in dynamic networks and at critical loads <small>Andrews, Jung, & Sioyiar</small>			Tight bounds for asynchronous randomized consensus <small>Hagit Attiya & Keren Censor</small>						Iteratively constructing preconditioners via the conjugate gradient method <small>Nicholas Harvey & John Dunagan</small>			On the convergence of Newton's method for monotone systems of polynomial equations <small>Kiefer, Lutenberger, & Esparza</small>			A combinatorial, primal-dual approach to semidefinite programs <small>Sanjeev Arora & Satyen Kale</small>			One sketch for all: fast algorithms for compressed sensing <small>Gilbert, Strauss, Tropp, & Vershynin</small>											

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ROOMS

ECOM Tutorial: Royal Palm 1 & 2
ISCA Session A: Golden West & California
ISCA Session B: San Diego

NE-IBC: Royal Palm 3 & 4
PLDI: Town & Country
SPAA: Pacific 3

STOC Session A: Golden Ballroom
STOC Session B: Pacific Salons 1 & 2

Tuesday Morning, June 12

	8:15	8:20	8:30	8:40	8:50	9:00	9:10	9:20	9:30	9:40	9:50	10:00	10:10	10:20	10:30	10:40	10:50	11:00	11:10	11:20	11:30 - 12:30		
DEECS						Opening	Keynote: Juhnyoung Lee <i>Service Science, Management, and</i>					COFFEE BREAK	BestChoice SRM: A simple and practical supplier relationship management system for e-procurement Lee, Kang, & Lee		Diversification of risk based on divided tasks in large-scale software system manufacture Tokuro Matsuo & Yoshihito Saito						PLENARY TALK Grand Exhibit Hall <i>Wireless Sensing -</i> <i>The Internet's Front-Tier</i> David Culler, UC-Berkeley Deborah Estrin, UCLA		
*ECOM	*Tutorial 3: <i>Modeling Complex Networks for Electronic Commerce</i>																						
ISCA	A 64-bit stream processor architecture for scientific applications Yang, Yan, Xing, Deng, Jiang, & Zhang				Physical simulation for animation & visual effects: Parallelization & characterization for chip multiprocessors Hughes, Grzeszczuk, Sifakis, et al.				ParallAX: An architecture for real-time physics Yeh, Faloutsos, Patel., & Reinman				COFFEE BREAK	Architectural implications of brick & mortar silicon manufacturing Kim, Mehrara, Oskin, & Austin		AquaCore: A programmable architecture for microfluidics Amin, Thottethodi, Vijaykumar, Wereley, et al.							
														Mechanisms for store-wait-free multiprocessors Wenisch, Ailamaki, Falsafi, Moshovos		BulkSC: Bulk enforcement of sequential consistency Ceze, Tuck, Montesinos, & Torrellias							
*Metrics	*Tutorial 1: <i>A Survey of Modern Cryptography</i>																						
*MineNet						Welcome	Keynote: Albert Greenberg <i>Inference in Network and Systems Management</i>					COFFEE BREAK	A three-tier IDS via data mining approach Hwang, Lee, & Lee		Identifying and tracking suspicious activities through IP gray space analysis Jin, Zhang, Xu, Cal, & Sahu		ANEX: A density-based measure for anomaly exposure Brauckhoff, et al						
PLDI	Optimistic parallelism requires abstractions Kulkarni, Pingali, et al.		Behavior-oriented parallelism Ding, Shen, Kelsey, et al.		Effective automatic parallelization of stencil computations Krishnamoorthy et al.		A race and transaction-aware runtime for Java Elmas, Tasiran, & Qadeer		COFFEE BREAK		Shape analysis & inductive recursion synthesis Guo, Vachharahani & August		Thread-modular shape analysis Gotsman, Berdine, Cook et al.		Making context-sensitive points-to analysis with heap cloning practical Lattner, Lenharth, & Adve		The ant and the grasshopper: Fast & accurate pointer analysis for... Hardekopf & Lin						
STOC					The condition number of a randomly perturbed matrix Van Vu & Terence Tao		Balanced allocations: The weighted case Ude Wieder & Kunal Talwar		Towards 3-query locally decodable codes of subexponential length Sergey Yekhanin		COFFEE BREAK		Tight integrality gaps for Lovasz-Schrijver LP relaxations of vertex cover and max cut Schoenebeck, Trevisan, & Tulsiani		Rank complexity gap for Lovasz-Schrijver and Sherali-Adams proof systems Stefan Dantchev								
					Circuit lower bounds for Merlin-Arthur classes Rahul Santhanam		Interpolation of depth-3 arithmetic circuits with two multiplication gates Amir Shpilka		Separating AC ⁰ from depth-2 majority circuits Alexander Sherstov				Linear probing & constant independence Pagh, Pagh, & Ruzic		Optimal suffix selection Franceschini & Muthukrishnan								
WPM			INTRO	Rational expectations at the racetrack ... using prediction market prices Amit Gandhi		Strategic foundations of prediction markets & the efficient markets hypoth. Ricardo Serrano-Padial		Discussant	Q&A	Mini Panel <i>Political Election</i>		COFFEE BREAK	Information aggregation mechanisms & environmental complexity Healy, Ledyard, Linnardi, et al.		A field experiment on monetary incentives in prediction markets Stefan Luckner		Discussant	Q&A					

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ROOMS

DEECS:	Royal Palm 5 & 6	Metrics Tutorial	Pacific Salon 6 & 7	STOC Session A:	Golden Ballroom
ECOM Tutorial:	Royal Palm 1 & 2	MineNet:	Pacific Salon 4 & 5	STOC Session B:	Pacific Salons 1 & 2
ISCA A:	Golden West & California	PLDI:	Town & Country	WPM:	Royal Palm 3 & 4
ISCA B:	San Diego				

Tuesday Afternoon, June 12

	1:30	1:40	1:50	2:00	2:10	2:20	2:30	2:40	2:50	3:00	3:10	3:20	3:30	3:40	3:50	4:00	4:10	4:20	4:30	4:40	4:50	5:00	5:10	5:20	5:30	5:40	5:50		
DEECS				DeepBot: A focused crawler for accessing hidden web content Diaz, Santiago, Bermudez, et al.		Forensic computing system using a DRM technique Wol Young Lee		Analyzing BPEL business processes based on Petri net Dun, Zhao, Zhang, & Wang		COFFEE BREAK					Short-time approximation on combinatorial auctions: A comparison on approx. winner determination algorithms Naoki Fukuta & Takayuki Ito		Negotiation model of the ICS environment Martins, Labidi, & Maia		Panel: Issues in Data Engineering for E-Commerce and Services Research			Wrap-Up & Closing Remarks							
*ECOM	*Tutorial 4: Sponsored Search																												
ISCA				Limiting the power consumption of main memory Diniz, Guedes, Meira, & Bianchini		Power model validation through thermal measurements Mesa-Martinez, Nayfach-Battilana, et al.		Thermal modeling and Management of DRAM memory systems Lin, Zheng, Zhu, David, & Zhang		COFFEE BREAK					Comparing memory systems for chip multiprocessors Leverich, Arakida, Solomatnikov, et al.		Interconnect design considerations for large NUCA caches Muralimanohar & Balasubramanian		Adaptive insertion policies for high performance caching Qureshi, Jaleel, Patt, Steely, & Emer										
				ReCycle: Pipeline adaptation to tolerate process variation Tiwari, Sarangi, & Torrellas		Matrix scheduler reloaded Sassone, Rupley, Brekelbaum, Loh, & Black		Late-binding: enabling unordered load-store queues Sethumadhavan, Roesner, Emer, et al.				Performance and security lessons learned from virtualizing the Alpha processor Paul Karger		Automated design of application specific superscalar processors: An analytical approach Tejas Karkhanis & James Smith		Analysis of redundancy & application balance in the SPEC CPU2006 benchmark suite Phansakar, Joshi, & John													
*Metrics	*Tutorial 2: NetFPGA: An Open-Source Hardware Platform for Networking Research & Teaching																		BREAK		*Tutorial 3: Network Data Streaming - Algorithmic Techniques for Managing Massive Data Sets of the Internet (5:00-6:30)								
*MineNet	Invited Talk: Nina Taft Anomaly Detection in Large Networks Using Approximation Techniques			Authentication anomaly detection: A case study on a VPN Chapple, Chawla, & Striegel		Building a prototype for network measurement virtual observatory Matray, Csabai, Haga, et al.		COFFEE BREAK					A Markovian signature-based approach to IP traffic classification Dahmouni, Valon, & Rosse		Byte Me: A case for byte accuracy in traffic classification Erman, Mahanti, et al.		SIP-based VoIP traffic behavior profiling and its applications Kang & Zhang		Real-time monitoring of SIP infrastructure using message classification Acharya, Banerjee, et al.										
PADS	*Tutorial: Parallel and Distributed Simulation: Traditional Techniques & Recent Advances																												
PLDI	Path Invariants Beyer, Henzinger, et al.		Ditto: Automatic incrementalization of data struct. invariant checks Shankar & Bodik		Proving thread termination Cook, Podelski, et al.		Regularly annotated set constraints Kodumal & Aiken		Efficient static analysis of XML paths & types Geneves, Layaada, et al.		COFFEE BREAK					The ExoVM system for automatic VM and application reduction Titzer, Auerbach, et al.		Offline compression for on-chip RAM Coopridr & Regehr		Online optimization driven by hardware performance monitoring Schneider, Payer, & Gross		UCC: Update-conscious compilation for energy efficiency in sensor nets Li, Zhang, Yang, & Zheng		Profile-driven energy reduction in network-on-chips Li, Chen, Kandemir, & Kolcu		Parameterized tiled loops for free Renganarayana, Kim, et al.			
SSRE	Panel: Research Ethics 5:50 - 7:00																												
STOC				Limitations of VCG-based mechanisms Stahar Dobzinski & Noam Nisan		The communication complexity of uncoupled Nash equilibrium procedures Sergiu Hart & Yishay Mansour		Proportional response dynamics lead to market equilibrium Fang Wu & Li Zhang		Eisenberg-Gale markets: algorithms & structural properties Kamal Jain & Vijay Vazirani		COFFEE BREAK					Parallel repetition: Simplifications and the no-signaling case Thomas Holenstein		An efficient parallel repetition theorem for Arthur-Merlin games Pass & Venkatasubramanian		Verifying & decoding in constant depth Goldwasser, Gutfreund, & Healy		Low-end uniform hardness vs. randomness tradeoffs for AM Ronit Shaltiel & Christopher Umans			Student Poster Talks 5:45 - 6:15			
				Interval completions with few edges Heggernes, Paul, Telle, et al.		Computing crossing number in linear time Kawarabayashi & Reed		Terminal backup, 3D matching, & covering cubic graphs Anselevich & Karagiozova		Holographic algorithms: From art to science Jin-Yi Cai & Pinyan Lu		Randomly coloring planar graphs with fewer colors than the maximum degree Hayes, Vera, & Vigoda		Inapproximability of the Tutte polynomial Leslie Goldberg & Mark Jerrum		Tensor-based hardness of the shortest vector problem to within almost polynomial factors Ishay Haviv & Oded Regev		Lattices that admit logarithmic worst-case to average-case connection factors Chris Peikert & Alon Rosen											
WPM	Using virtual stock exchanges to forecast box-office revenue Jank & Foutz		Risk aversion, beliefs, & prediction market equilibrium Stefan Gjerstad		Discussant		Q&A		Communication strategy with information markets Glienke, Ankenbrand, et al.		The hidden beauty of the quadratic market scoring rule Michael Abramowicz		Discussant		Q&A		COFFEE BREAK		Non-myopic strategies in prediction markets Dimitrov & Sami		Bluffing and strategic reticence in prediction markets Chen, Reeves, et al.		Discussant		Q&A		Industrial Panel		

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ROOMS

DEECS: Royal Palm 5 & 6
ECOM Tutorial: Royal Palm 1 & 2
ISCA Session A: Golden West & California
ISCA Session B: San Diego

Metrics Tutorial: Pacific Salon 6 & 7
MineNet: Pacific Salon 4 & 5
PADS Tutorial: Terrace 1
PLDI: Town & Country

SSRE: Pacific Salon 3
STOC Session A: Golden Ballroom
STOC Session B: Pacific Salons 1 & 2
WPM: Royal Palm 3 & 4

Wednesday Morning, June 13

	8:15	8:20	8:30	8:40	8:50	9:00	9:10	9:20	9:30	9:40	9:50	10:00	10:10	10:20	10:30	10:40	10:50	11:00	11:10	11:20	11:30 - 12:30	
COLT			Minimax bounds for active learning Rui Castro & Robert Nowak	Stability of k-means clustering Ben-David, Pal, & Simon	Margin based active learning Balcan, Broder, & Zhang	COFFEE BREAK		Learning large-alphabet & analog circuits w. value injection queries Angluin, Aspnes, Chen, & Reyzin	Teaching dimension & the complexity of active learning Steve Hanneke	Multi-view regression via canonical correlation analysis Sham Kakade & Dean Foster												
Complex													WELCOME	On derandomizing probabilistic sublinear-time algorithms Marius Zimand								
ECOM		Beyond Moulou mechanisms Mehta & Roughgarden	Approximately maximizing efficiency & revenue in convex environments Nguyen & Tardos	Generalized trade reduction mechanisms Gonen, Gonen, & Pavlov	Worst-case optimal redistribution of VCG payments Guo & Conitzer	COFFEE BREAK		Budget opt. in search-based ad auctions Feldman et al.	Analysis of rank rules in keyword auctions Lahaie et al.	Dynamic price competition: advertising auctions Michael Zhang	Impact of recommen. systems on sales divers. Hosanagar++											
ExpCS	Keynote: David Johnson <i>What is the Science in Experimental Computer Science?</i>					COFFEE BREAK		Quantifying the cost of a context switch Li, Ding, & Shen	Context switch overheads for Linux on ARM platforms David, Carlyle, & Campbell	The context-switch overhead inflicted by hardware interrupts (and the enigma of do-nothing loops) Dan Tsafir												
ISCA		VPC prediction: Reducing the cost of indirect branches via hardware-based... Kim, Joao, Mutlu, Lee, Patt, & Cohn	Ginger: Control independence using tag rewriting Andrew Hilton & Amir Roth	Transparent control independence (TCI) Al-Zawawi, Reddy, Rotenberg, et al.	COFFEE BREAK		Raksha: A flexible information flow architecture for software security Dalton, Kannan, Kozyrakis	New cache designs for thwarting software cache-based side channel attacks Zhenghong Wang & Ruby Lee														
			Examining ACE analysis in reliability estimates using fault-injection Wang, Mahesri, & Patel	Configurable isolation: Building high availability systems w. multi-core proc. Aggarwal, Ranganathan, Joupji, et al.	COFFEE BREAK		Mechanisms for bounding vulnerabilities of processor structures Soundrarajan, Parashar, & Sivasubramanian	Dynamic prediction of architectural vulnerability from microarchitectural state Walcott, Humphreys, & Gurumurthi														
*MAMA		Opening Remarks	Poisson-noise suppression in self-similarity studies based on packet counting Gabriele Gianini & Ernesto Damiani	Towards understanding of complex communication networks: Performance phase transitions & control Vladimir Marbukh	COFFEE BREAK		Accuracy of measured throughputs & mean response times Taka Osogami	Quantifying the effect of higher moments of job size distribution on the performance of an M/G/s queueing system Gupta, Harchol-Balter, Dai, & Zwart														
PADS		Opening Remarks	Keynote: Michael Zyda <i>A Look at the Future of Networked & Mobile Environment</i>		COFFEE BREAK		Smallpox over San Diego: Joint real-time federations of distrib. Simulations... Linebarger, Goldsby, Fellig, Hawley, et al.	Ad hoc distributed simulations Fujimoto, Hunter, Sirichoke, Palekar, et al.														
PLDI	Towards locating execution omission errors Zhang, Tallam, Gupta & Gupta	Searching for type-error messages Lerner, Flower, et al.	Static error detection using semantic inconsistency inference Dillig, Dillig, & Aiken	Iterative context bounding for systematic testing of multithreaded programs Musuvathi & Qadeer	COFFEE BREAK		CGCExplorer: A semi-automated search procedure for concurrent collectors Vechev, Yahav, Bacon, et al.	A general framework for certifying garbage collectors and their mutations McCreight, Shao, Lin & Li	Practical memory leak detection using guarded value-flow analysis Cherem, Princehouse, et al.													
*SSW	Poster Session																					
STOC		Property testing in hypergraphs & the removal lemma Vojtech Rodl & Mathias Schacht	Testing k-wise & almost k-wise independence Alon, Andoni, Kaufman, et al.	Loiw-degree tests at large distances Alex Samorodnitsky	COFFEE BREAK		Playing games with approximation algorithms Kakade, Kalai, & Ligett	Reordering buffers for general metric spaces														
		Exponential separations for 1-way quantum comun. Complexity Gavinsky, Kempe, Kerenidis, et al.	Negative weights make adversaries stronger Hoyer, Lee, & Spalek	On the impossibility of a quantum sieve alg. for graph isomorphism Moore, Russell, & Sniady	COFFEE BREAK		Toward a general theory of quantum games Gus Gutoski & John Watrous	Search via quantum walk Magniez, Nayak, Roland, & Santha														

PLENARY TALK
Grand Exhibit Hall

The Art of Reduction

Avi Wigderson
Institute for Advanced Study

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ROOMS

COLT: Pacific Salon 3	ISCA A: Golden West & California	PLDI: Town & Country
Complex: Royal Palm 1-4	ISCA B: San Diego	SSW: Pacific Salons 6 & 7
ECOM: Sunrise	MAMA: Pacific Salons 4 & 5	STOC Session A: Golden Ballroom
ExpCS: Sheffield	PADS: Sunset	STOC Session B: Pacific Salons 1 & 2

Wednesday Afternoon, June 13

	1:30	1:40	1:50	2:00	2:10	2:20	2:30	2:40	2:50	3:00	3:10	3:20	3:30	3:40	3:50	4:00	4:10	4:20	4:30	4:40	4:50	5:00	5:10	5:20	5:30	5:40	5:50		
COLT	Aggregation by exponential weighting & sharp oracle inequalities <small>Amak Dalalyan & Alexandre Tsybakov</small>			Occam's Hammer <small>Gilles Blanchard & Francois Fleuret</small>		Resampling-based conf. regions & mult. tests for a corr. rand. vector <small>Arlot, Blanchard, & Roquain</small>		Suboptimality of penalized risk minimization in classification <small>Guillaume Lecue</small>		Transductive Rademacher complexity & its applications <small>Ran El-Yaniv & Dmitry Pechyon</small>			COFFEE BREAK		U-shaped, iterative, & iterative-with-counter learning <small>John Case & Samuel Moelius III</small>		Mind change optimal learning of Bayes net structure <small>Schulte, Luo, & Greiner</small>		Learning correction grammars <small>Carlucci, Case, & Jain</small>		Mitotic classes <small>Sarjay Jain & Frank Stephan</small>								
Complex		The communication complexity of correlation <small>Harsha, Jain, McAllester, & Radhakrishnan</small>			On computation & communication with small bias <small>Buhrman, Vereshchagin, & de Wolf</small>			Lower bounds for multi-player pointer jumping <small>Amit Chakrabarti</small>		COFFEE BREAK		Low-depth witnesses are easy to find <small>Antunes, Fortnow, Pinto, & Souto</small>		Bounded queries and the NP machine hypothesis <small>Richard Chang & Suresh Purini</small>		An exponential LB on the size of constant-depth threshold circuits w. small energy complexity <small>Kei Uchizawa & Eiji Takimoto</small>													
ECOM			Shake it up baby: scheduling with package auctions <small>Takeuchi, Lin, Chen, & Finholt</small>		Evaluating compout cri-tiquing recommenders <small>Reilly, Zhang, McGinty, et al.</small>		Indirect partner interaction in P2P networks: stimulating.. <small>Schossner, Bohm, & Vogt</small>		Understanding user behavior in online feedback.. <small>Arjun Talwar & Bodo Vogt</small>		COFFEE BREAK		On threshold behavior in query incentive networks <small>Arcaute, Kirsh, Kumar, et al.</small>		The role of compatibility in diffusion of tech. thru social networks <small>Immorlica, Kleinberg, Mahdian et al</small>		Strong equilibrium in cost sharing connection games <small>Epstein, Feldman, & Mansour</small>		Stackelberg thresholds in network routing games: value of altruism <small>Sharma & Williamson</small>		Congestion games with malicious players <small>Babaioff, Kleinberg, et al.</small>								
ExpCS	Analysis of input-dependent program behavior using active profiling <small>Shen, Zhang, Ding, Scott, Dwarkadas, et al.</small>			Quantifying the performance isolation properties of virtualization systems <small>Matthews, Hu, Hapuarachchi, Deshane, et al.</small>			An analysis of XML compression efficiency <small>Augeri, Mullins, Baird, Bulutoglu, & Baldwin</small>		Towards user-centric metrics for denial-of-service measurement <small>Mirkovic, Hussain, Wilson, Fahmy, et al.</small>			COFFEE BREAK		Education Roundtable															
LCTES			Opening		A type system for preventing data races & deadlocks in the JVM language <small>Permandla, Roberson, & Boyapati</small>			Automated fault localization w. statistically suspicious program states <small>Chou, Huang, Tsai, & Chen</small>			COFFEE BREAK		Frequency-aware system-wide energy optimization for real-time tasks <small>Xiliang Zhong & Cheng-Zhong Xu</small>		DVSlack: combining leakage reduction & voltage scaling in feedback EDF sched. <small>Xifan Zhu & Frank Mueller</small>		Compiler-based approach for integrated CPU and L2 cache voltage scaling <small>AbouGhazaleh, Ferreira, Liberato, et al.</small>												
*MAMA	Modelling & evaluation of an online TV recording service <small>Tobias Hossfeld & Kenji Leibnitz</small>		Capacity optimization in wireless mesh networks <small>Peng Wang & Stephan Bohacek</small>		Exponential supermartingales for evaluating end-to-end backlog bounds <small>Florin Ciucu</small>		Insensitivity properties for PS server farms with JSQ routing <small>Gupta, Harchol-Balter, Sigman, & Whitt</small>			COFFEE BREAK		Characterization of moments & autocorrelation in MAPs <small>Casale, Zhang, & Smirni</small>		Approximate analysis of a network of fluid queues <small>Tony Field & Pete Harrison</small>		The time-correlated update problem <small>Reich, Misra, & Rubinstein</small>		Product form stationary distributions for diffusion approxs. ... <small>Kang, Kelly, Lee, & Williams</small>		<i>Plus one more talk. See below.</i>									
*METRICS												*Tutorial 4: Network Coding (4:00 - 6:30)																	
PADS	OPENING	A second-order accurate Adams-Bashforth type discrete event integration scheme <small>James Nutaro</small>		Comparing synchronous and asynchronous variable step size explicit ODE solvers: A simulation study <small>Fernando Barros</small>		Stability of Asynchronous variational integrators <small>Fong, Darve, & Lew</small>		A generic pattern for modifying traditional PDE solvers to exploit heterogeneity in asynchronous behavior <small>Jammalamadaka & Zeigler</small>			COFFEE BREAK		Adaptive support for range queries via push-pull algorithms <small>Rob Minson & Georgios Theodoropoulos</small>		The multi-agent data collection in HLA-based simulation system <small>Song, Miao, Shen, Tan, & Zhao</small>		An efficient sort-based DDM matching algorithm for HLA applications with a large spatial environment <small>Pan, Turner, Cai, & Li</small>		Business Meeting										
PASTE		Opening Remarks	Evaluating static analysis defect warnings from prod. Software <small>Ayewah, Pugh, Morgenthaler, et al.</small>		Finding more null pointer bugs, but not too many <small>David Hovemeyer & William Pugh</small>		Group Presentation: <i>Introducing natural language prgram analysis</i> <small>Lori Pollock, K. Vjay-Shankar, David Shepherd, Emily Hill, Zachary Fry, & Kishem Maloor</small>			COFFEE BREAK		Interstatement must aliases for data depend. anal. of heap locs. <small>Qian, Xu, & Min</small>		Light context-sensitive points-to analysis for Java <small>Ana Milanova</small>		Heap analysis in the presence of collection libraries <small>Marron, Stefanovic, et al.</small>		Comparing tall graphs <small>Ondrej Lhotak</small>											
*SSW	*Panel: Performance Evaluation: An Industry Perspective																												
STOC			All-pairs bottleneck paths for gen. graphs in truly sub-cubic time <small>Vassilevska, Williams, & Yuster</small>		More algorithms for all-pairs shortest paths in weighted graphs <small>Timothy Chan</small>		Some new results on node-capacitated packing of A-paths <small>Gyula Pap</small>		A Gomory-Hu tree construction alg. for unweighted graphs <small>Bhulgat, Hariharan, Panigrahi, et al.</small>		COFFEE BREAK		Survivable network design with degree or order constraints <small>Lao, Naor, Salavatipour, & Singh</small>		Approximating min. bounded degree spanning trees to within 1 <small>Mohit Singh & Lap Chi Lau</small>		Improved approximation for directed multicut & sparsest cut <small>Agarwal, Alon, & Charikar</small>		Deg. constrained network flows <small>Donovan, Shepherd, Vetta, et al.</small>										
			Uncertainty principles, extractors, & exp. embeddings of L2 into L1 <small>Piotr Indyk</small>		Vert. cuts, rand. walks & dimension reduct. in series-parallel graphs <small>Brinkman, Karagiozova, & Lee</small>		Local embeddings of metric spaces <small>Abraham, Bartal, & Neiman</small>		Sampling based dim. Reduction for subspace approximation <small>Deshpande & Varadarajan</small>		COFFEE BREAK		Lower bounds for randomized read/write stream algorithms <small>Beame, Jayram, & Rudra</small>		Lower bounds in comm. Complexity based on factorization norms <small>Adi Shraibman & Nati Linial</small>		Constructing non-computable Julia sets <small>Braverman & Yampolsky</small>												
VEE		Welcome	Cloneable JVM: A new approach to start isolated Java applications faster <small>Kawachiya, Ogata, Silva, Onodera, et al.</small>		Automatic feedback-directed object inlining in the Java HotSpot virtual machine <small>Christian Wimmer & Hanspeter Mossenbock</small>		Speculative optimization using hardware monitored guarded regions for Java virtual machines <small>Lixin Su & Mikko Lipasti</small>			COFFEE BREAK		VMM independent graphics acceleration <small>Lagar-Cavilla, Tolia, Satyanarayanan, et al.</small>		Libra: A library operating system for a JVM in a virtualized execution environment <small>Ammons, Appavoo, Butrico, Da Silva, et al.</small>		FoxyTechnique: Tricking operating system policies with a virtual machine monitor <small>Hiroshi Yamada & Kenji Kono</small>													

* Open only to those who registered for this event

ROOMS

COLT: Pacific Salon 3
Complex: Royal Palm 1-4
ECOM: Sunrise
ExpCS: Sheffield

LCTES: California
MAMA: Pacific Salons 4 & 5
METRICS Tutorial: Pacific Salons 6 & 7
PADS: Sunset

PASTE: Royal Palm 5 & 6
SSW: Pacific Salons 6 & 7
STOC Session A: Golden Ballroom
STOC Session B: Pacific Salons 1 & 2
VEE: Terrace Salon 1-3

Late MAMA talk (6:00 - 6:30)

Optimal Capacity Planning in Stochastic Loss Networks
 Lu, Radovanovic, & Squillante

Thursday Morning, June 14

	8:15	8:20	8:30	8:40	8:50	9:00	9:10	9:20	9:30	9:40	9:50	10:00	10:10	10:20	10:30	10:40	10:50	11:00	11:10	11:20	11:30 - 12:30	
COLT			Regret to the best vs. regret to the average Even-Dar, Kearns, Mansour, et al.		Strategies for prediction under imperfect monitoring Lugosi, Mannor, & Stoltz			Bounded parameter Markov decision processes with average reward criterion Ambuj Tewari & Peter Bartlett		COFFEE BREAK			On-line estimation with the multivariate Gaussian distribution Sanjoy Dasgupta & Daniel Hsu		Generalized entropy & asymptotic complexities of languages Kalnishkan, Vovk, & Vyugin		Q-learning with linear function approximation Francisco Melo & Isable Ribeiro					PLENARY TALK Grand Exhibit Hall <i>Designing by Accident</i> Guy Steele Sun Microsystems
Complex					Time-space tradeoffs for counting NP solutions modulo integers Ryan Williams			Halfspace matrices Alexander Sherstov		COFFEE BREAK					Unbalanced expanders and randomness extractors from Parvaresh-Vardy codes Venkatesan Guruswami, Christopher Umans, & Salil Vadhan							
ECOM			Trading networks with price-setting agents Blume, Easley, Kleinberg, et al.		On the complexity of combinatorial auctions: Structured item graphs & .. Gottlob & Greco		Selecting good Nash equilibria in graphical games Elkind, Goldberg, & Goldberg		Stochastic packing-market planning Daniel Golovin		COFFEE BREAK			Red Opal: Product-feature scoring from reviews Scaffidi et al.	Equilibrium model for matching over time Kumar et al.	Collusion resistant, incentive compat. feedback payments Jurca & Faltings	Congestion games w. load-depend. failures Polukarov et al.					
ExpCS			The user in experimental computer system research Dinda, Memik, Dick, Lin, Mallik, Gupta et al.		Experimental economics & experimental computer science: A survey Jens Grossklags			COFFEE BREAK			High-contrast algorithm behavior: Observation, hypothesis, and experimental design Matthias Stallman & Franc Brglez		Performance testing of combinatorial solvers with isomorph class instances Franc Brglez & Jason Osborne		EXACT: The experimental algorithmics computational toolkit Hart, Berry, Heaphy, & Phillips							
LCES					Java takes flight: Time-portable real-time programming with Exotasks Auerbach, Bacon, Iercan, Kirsch, et al.		Java client ahead-of-time compiler for embedded systems Hong, Kim, Shin, Oh, Lee, Choi, & Moon		COFFEE BREAK			An implementation of lattice-based memory reuse in the source-to-source translator ROSE Alias, Baray, & Darté		Combining source-to-source transformations and processor instruction set extensions for design-space explor...								
METRICS			Opening Remarks			Modeling and improving data cache reliability Ismail Kadayif & Mahmut Kandemir		An arrival curve based approach for QoS guarantees in shared storage systems Gulati, Merchant, & Varman		QoS policy and architecture for cache/memory in CMP platforms Iyer, Zhao, Guo, Makineni, Newell, et al.		Modeling the relative fitness of storage Mesnier, Wachs, Sambasivan, et al.										
PADS					Asynchronous event-driven particle algorithms Aleksander Donev		Formal validation of asynchronous interaction-agents algorithms for reaction-diffusion problems Redou, Kerdelo, Desmeulles, Abgrall, et al.		COFFEE BREAK			Macro-micro economic system simulation Onggo, Kusano, & Sato		Explanation exploration: Exploring emergent behavior Gore, Reynolds, Tang, & Brogan								
PASTE			Group Presentation: <i>An Overview of the Saturn Project</i> Alex Aiken, Suhabe Bugrara, Isil Dillig, Thomas Dillig, Peter Hawkins, & Brian Hackett					COFFEE BREAK			Case study: Supplementing program analysis w. natural lang. analysis to improve reverse eng. Shepherd, Pollock, & Vijay-Shankar		Discovering accurate interclass test dependencies Weilei Zhang & Barbara Ryder		Refining buffer overflow detection via demand-driven path-sensitive analysis Wei Le & Mary Lou Soffa							
PLAS	OPENING	Jifclipse: Development tools for security-typed applications Hicks, King, & McDaniel		Towards usable information flow security in Java Mark Thober & Scott Smith		A domain-specific programming language for secure multiparty computation Janus Nielsen & Michael Schwartzbach		COFFEE BREAK			Quantitative analysis of leakage for multi-threaded programs Pasquale Malacaria & Han Chen		A simulation-based proof technique for dynamic information flow Stephen McCamant & Michael Ernst									
VEE					How to shadow every byte of memory used by a program Nicholas Nethercote & Julian Seward		Ciby!: An environment for language diversity on mobile devices Kagstrom, Grahn, & Lundberg		COFFEE BREAK			YETI: A gradually extensible trace interpreter Zaleski, Stoodley, & Brown		Dynamic compilation: The benefits of early investing Kulkarni, Arnold, & Hind								

ROOMS

COLT:	Pacific Salon 3	ExpCS:	Sheffield	PADS:	Sunset
Complex:	Royal Palm 1-4	LCES:	California	PASTE:	Royal Palm 5 & 6
ECOM:	Sunrise	METRICS:	Golden West	PLAS:	Esquire
				VEE:	Terrace Salon 1-3

Thursday Afternoon, June 14

	1:30	1:40	1:50	2:00	2:10	2:20	2:30	2:40	2:50	3:00	3:10	3:20	3:30	3:40	3:50	4:00	4:10	4:20	4:30	4:40	4:50	5:00	5:10	5:20	5:30	5:40	5:50
COLT			How good is a kernel when used as a similarity measure Nathan Srebro	Gaps in support vector optimization List, Hush, Scovel, & Steinwart	Learning languages with rational kernels Cortes, Kontorovich, & Mohri	Generalized SMO-style decomposition algorithms Nikolas List	COFFEE BREAK		Invited Talk: Dana Ron <i>Property Testing: A Learning Theory Perspective</i>						Learning nested half-spaces & uphill decision trees Adam Kalai	An efficient re-scaled perceptron algorithm for conic systems Belloni, Freund, & Vempala	<i>Plus Two More Talks. See below.</i>										
Complex		Perfect parallel repetition theorem for quantum XOR proof systems Cleve, Sifsttra, Unger, Upadhyay	Quantum versus classical proofs & advice Scott Aaronson & Greg Kuperberg	Quantum t-designs: t-wise independence in the quantum world Andris Ambainis & Joseph Emerson	COFFEE BREAK		Norms, XOR lemmas, & lower bounds for GF(2) polynomials and multipart protocols Emanuele Viola & Avi Wigderson	On approximate majority and probabilistic time Emanuele Viola	On C-degrees, H-degrees & T-degrees Wolfgang Merkle & Frank Stephan																		
ECOM		Greedy bidding strategies for keyword auctions Cary, Das, Edelman, et al.	Optimal delivery of sponsored search advertisements subject to budget constraints Abrams, Mendeleevitch, et al.	Mediators in position auctions Ashlagi, Monderer, et al.	Allocating online advertisement space with unreliable estimates Mahdian, Nazerzadeh, et al.	COFFEE BREAK		Pricing of partially compatible products Kemp, Meyerson, Solanki, et al.	Generalized value decomposition & structured multiatribute auctions Yagil Engel & Michael Wellman	Optimal combinatoric auctions for single-minded bidders John Ledyard	Algorithmic pricing via virtual valuations Chawla, Hartline, & Kleinberg	Truthful mechanism design for multi-dimensional scheduling via cycle monotonicity Lavi & Swamy															
ExpCS	Pipeline spectroscopy Puzak, Hartstein, Emma, Srinivasan, et al.	Measuring performance, power, & temperature from real processors Mesa-Martinez, Brown, et al.	Introducing entropies for representing program behavior & branch predictor performance Yokota, Ootsu, & Baba	Empirical performance assessment using soft-core processors on reconfigurable hardware Hough, Krishnamurthy, Chamberlain, et al.	COFFEE BREAK		RA: Research assistant for the computational sciences Daniel Ramage & Adam Oliner	An active approach to measuring routing dynamics induced by autonomous systems Lo, Chang, & Colitti	RiceNIC: A reconfigurable network interface for experimental research & education Jeffrey Shafer & Scott Rixner	Rapid file system development using ptrace Spillane, Wright, Sivathanu, & Zadok																	
LCTES		Optimistic coalescing for heterogeneous register architectures Ahn, Lee, & Paek	On the complexity of spill everywhere under SSA form Bouchez, Dart, & Rastello	Tetris: A new register pressure control technique for VLIW processors Welfeng Xu & Russell Tessier	Hierarchical real-time garbage collection Pizlo, Hosking, & Vitek	COFFEE BREAK		Issues and challenges in compiling for CBEA Kathryn O'Brien	Safe worst-case execution time analysis by abstract interpretation of executable code Daniel Kaestner	Enabling seamless mobility: An enablers, experiences, & tools perspective Jonathan Engelsma																	
METRICS	Facilitating focused internet measurements Wen, Triukose, & Rabinovich	Diagnosing network disruptions with network-wide analysis Huang, Feamster, Lakhina, & Xu	Understanding network delay changes caused by routing events Pucha, Zhang, Mao, & Hu	COFFEE BREAK		Two-phase routing, scheduling & power control for wireless mesh networks with variable traffic Kashyap, Sengupta, Bhatia, & Kodialam	A machine learning approach to TCP throughput prediction Mirza, Sommers, Barford, & Zhu	Sensitivity of PCA for traffic anomaly detection Ringberg, Soule, Rexford, & Diot	Backbone construction in selfish wireless networks Lee, Levin, Gopalakrishnan, & Bhattacharjee																		
PADS	Modeling and simulations of TCP MANET worms Abdelhatez, Riley, Cole, & Phamdo	GPU-accelerated evaluation platform for high fidelity network modeling Zhiguo Xu & Rajive Bagrodia	Parallel simulation of hybrid network traffic models Jason Liu	An abstract Internet topology model for simulating peer-to-peer content distribution LaFortune, Carothers, Smith, & Hartman	COFFEE BREAK		Local time warp: An impenation of performance analysis Hassan Rajael	An approach for incorporating rollback through perfectly reversible computation in a stream simulator David Bauer & Ernest Page	Using reversible computation techniques in a parallel simulation of a multiprocessor computing system Andriy Naborsky & Richard Fujimoto																		
PASTE	AWE: Improving software analysis through modular integration of static and dynamic analyses Brown, Khazan, & Shovich	Dynamic purity analysis for Java programs Xu, Pickett, & Verbrugge	Group Presentation: <i>Improving Software Quality with Static Analysis</i> Jeffrey Foster, Michael Hicks, & William Pugh		COFFEE BREAK		Group Presentation: <i>Enforcing and Validating User-Defined Programming Disciplines</i> Brian Chin, Daniel Marino, Shane Markstrum, & Todd Millstein		Concluding Remarks																		
PLAS	Guarded models for intrusion detection Hassen Saidi	Cautious virus detection in the extreme Case et al.	Localized delimited release: Combining the what and where dimensions of information release Aslan Askarov & Andrei Sabelfeld	Towards a logical account of de-classification Banerjee et al.	Fast probabilistic simulation, non-termination, & secure information flow Smith et al.	A trust management perspective on managing policy updates... Bandhakavi et al.	COFFEE BREAK		Large-scale analysis of format string vulnerabilities in Debian Linux Karl Chen & David Wagner	Using web application construction frameworks to protect against code injection attacks Benjamin Livshits	Finding bugs in Bash scripts Karl Mazurak & Steve Zdanecwic																
VEE		Virtual I/O scheduler: A scheduler of schedulers for performance virtualization Seetharami Seelam & Patricia Teller	I/O processing in virtualized platform: A simulation-driven approach Chadha, Illikkal, Iyer, Moses, Newell et al.	Xen and Co: Communication-aware CPU scheduling for consolidated Xen-based hosting platforms Govindam, Nath, Das, Uргаonkar, et al.	COFFEE BREAK		PinOS: A programmable framework for whole-system dynamic instrumentation Bungale & Luk	Metadata driven memory optimizations in dynamic binary translator Xu, Li, Bao, Wang, & Huang																			

ROOMS

COLT: Pacific Salon 3
Complex: Royal Palm 1-4
ECOM: Sunrise
ExpCS: Sheffield

LCTES: California
METRICS: Golden West
PADS: Sunset

PASTE: Royal Palm 5 & 6
PLAS: Esquire
VEE: Terrace Salon 1-3

Late COLT Talks (5:50 - 6:40)

A lower bound for agnostically learning distributions Klivans & Sherstov	Sketching information Divergences Guha, Indyk, & McGregor
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Friday Morning, June 15

	8:30	8:40	8:50	9:00	9:10	9:20	9:30	9:40	9:50	10:00	10:10	10:20	10:30	10:40	10:50	11:00	11:10	11:20	11:30 - 12:30
COLT			Competing with stationary prediction strategies Vladimir Vovk		Improved rates for the stochastic continuum-armed bandit problem Auer, Ortner, & Szepesvari		Learning permutations with exponential weights Helmhold & Warmuth			COFFEE BREAK			Multitask learning with expert advice Abernethy, Bartlett, & Rakhlin		Online learning with prior knowledge Hazan & Megiddo				PLENARY TALK Town & Country <i>Computer Science:</i> <i>Past, Present, & Future</i> Edward Lazowska University of Washington
Complex			Understanding parallel repetition requires understanding foams Feige, Kindler, & O'Donnell			The complexity of polynomials & their coefficient functions Guillaume Malod			COFFEE BREAK			A linear round lower bound for Lovasz-Schrijver SDP relaxations of vertex cover Schoenebeck, Trevisan, & Tulsiani							
ECOM	Creating algorithms for barter exchange markets: Enabling nationwide kidney exchanges Abraham, Blum, & Sandholm		Optimizing scrip systems: Efficiency, crashes, hoarders, & altruists Kash, Friedman, & Halpern		A strategic model for information markets Nikolova & Sami		Betting on permutations Chen, Fortnow, et al.			COFFEE BREAK			Frugality ratios & improved truthful mechanisms for VC Elkind et al.	Mechanisms for multi-unit auctions Dobzinski et al.	Approximating Nash equilibria using small-support strats. Feder et al.	Progress in approximate Nash equilibria Daskalakis++			
LCTES			Poster Session						COFFEE BREAK			Addressing instruction fetch bottlenecks by using an instruction register file Hines, Tyson, & Whalley		WCET analysis of instruction caches with prefetching Jun Yan & Wei Zhang					
METRICS	Scalability of fork/join queueing networks with blocking Xia, Liu, Towsley, & Lelarge			Optimizing system configurations quickly by guessing at the performance Takayuki Osogami & Sei Kato			Sizing L1 sketches: A rank-based analysis Wang, Lv, Josephson, Dong, Charikar, & Li			COFFEE BREAK			PBS: A unified priority-based scheduler Feng, Misra, & Rubenstein		Adaptive and scalable comparison scheduling Jelenkovic, Kang, & Tan				
	Managing energy performance tradeoffs for multithreaded applications on multiprocessor architectures Park, Jiang, Adve, & Zhou			An algorithm for approximate counting using limited memory resources Andrej Cvetkovski			Synthetic designs: A new form of true experimental design for use in information systems development Eric Lee & Thom Whalen												
PADS			SimBA: A discrete event simulator for performance prediction of volunteer computing projects Taufel, Kerstens, Estrada, Flores, & Teller			A federated simulation environment for hybrid systems Gayen, Tyson, Franklin, & Chamberlain			COFFEE BREAK			A design-driven partitioning algorithm for distributed Verilog simulation Lijun Li & Carl Tropper		A flexible dynamic partitioning algorithm for optimistic distributed simulation Peschlow, Honecker, & Martini					
VEE			Nomad: migrating OS-bypass networks in virtual machines Liu, Koop, Abali, & Panda			Live wide-area migration of virtual machines including local persistent state Bradford, Kotsovinos, Feldmann, et al.			COFFEE BREAK			Practical escape analyses: How good are they? Lee, Fang, & Midkiff		Reflexes: Abstractions for highly responsive systems Spring, Vitek, Pizlo, & Guerraoui					

ROOMS

COLT: Pacific Salon 3
Complex: Royal Palm 1-4
ECOM: Sunrise
LCTES: California

METRICS Session A: Golden West
METRICS Session B: San Diego
PADS: Sunset
VEE: Terrace Salon 1-3

Friday Afternoon, June 15

	1:30	1:40	1:50	2:00	2:10	2:20	2:30	2:40	2:50	3:00	3:10	3:20	3:30	3:40	3:50	4:00	4:10	4:20	4:30	4:40	4:50	5:00	5:10	5:20	5:30	5:40	5:50
COLT			Nonlinear estimators & tail bounds for dimension reduction in L1 using Cauchy random projs. Li, Hastie, & Church			Sparse density estimation with L1 penalties Bunea, Tsybakov, & Wegkamp		L1 regularization in infinite dimensional feature spaces Rosset, Swirszcz, Srebro, & Zhu			Prediction by categorical features; Generalization properties & applications to feature ranking Sabato & Shalev-Schwartz		COFFEE BREAK			Invited Talk: Santosh Vempala <i>Spectral Algorithms for Learning & Clustering</i>					Observational learning in random networks Lorenz, Marcinişzyn, & Steger	The loss rank principle for model selection Marcus Hutter	<i>Plus One More Talk. See below.</i>				
Complex			Directed planar reachability is in unambiguous logspace Bourke, Tewari, & Vinodchandran		Parity problems in planar graphs Braverman, Kulkarni, & Roy			S-T connectivity on digraphs with known stationary distribution Chung, Reingold, & Vadhan				COFFEE BREAK			On parameterized path & chordless path problems Yijia Chen & Jorg Flum		Testing properties of constraint graphs Halevy, Lachish, Newman, & Tsur			Efficient arguments without short PCs Ishai, Kushilevitz, & Ostrovsky							
LCTES			External memory page remapping for embedded multimedia systems Ke Ning & David Kaeli		Dynamic data scratchpad memory management for a memory subsystem with an MMU Cho, Egger, Lee, & Shin			Scratchpad allocation for data aggregates in superperfect graphs Li, Nguyen, & Xue		SWL: A search-while-load demand paging scheme with NAND flash memory In, Shin, & Kim				COFFEE BREAK			Optimizing software cache performance of packet processing applications Wang, Zhang, & Zang		Compiler managed partitioned data caches for low power Ravindran, Chu, & Mahke								
METRICS	Optimal capacity planning in stochastic loss networks with time-varying workloads Bhadra, Lu, & Squillante		Flow-level stability of data networks with non-convex and time-varying rate regions Liu, Proutiere, Yi, Chung, & Poor			Panel: Future Research Directions							COFFEE BREAK			Poster Session											
VEE			Closing Remarks & Award Presentations																								

Saturday Morning, June 16

	8:30	8:40	8:50	9:00	9:10	9:20	9:30	9:40	9:50	10:00	10:10	10:20	10:30	10:40	10:50	11:00	11:10	11:20	11:30	11:40	11:50	12:00	12:10	12:20	12:30	12:40	12:50
Complex			Bases collapse in holographic algorithms Jin-Yi Cai & Pinyan Lu			On the theory of matchgate computations Cai, Choudhary, & Lu				COFFEE BREAK			A new interactive hashing theorem Ittach Haitner & Omer Reingold			Limits on the hardness of lattice problems in Lp norms Chris Peikert			On heuristic time hierarchies Konstantin Pervyshev								
METRICS	Wire speed packet classification without TCAMs: A few more registers (and a bit of logic) are enough Dong, Banerjee, Wang, & Agarwal		Data layouts for object-oriented programs Martin Hirzel			Building high accuracy bloom filters with smart hash function choice Hao, Kodialam, & Lakshman			An analysis of latent sector errors in disk drives Bairavasundaram, Goodson, Pasupathy, et al.				COFFEE BREAK			Clustering and sharing incentives in BitTorrent systems Legout, Liogkas, Kohler, & Zhang		Distributed link scheduling with constant overhead Sanghavi, Bui, & Srikant		Theoretical bounds on control-plane self-monitoring in routing protocols Rajendran, Misra, & Rubenstein			Oblivious routing for fat-tree based system area networks with uncertain traffic demands Yuan, Nienaber, Duan, & Melhem				

ROOMS

COLT: Pacific Salon 3
Complex: Royal Palm 1-4
LCTES: California

METRICS: Golden West
VEE: Terrace Salon 1-3

Final METRICS Session
(1:00 - 1:15 Saturday)
CLOSING REMARKS

Late COLT Talk (5:50 - 6:15)

Robust reductions from ranking to classification
Balcan, Bansal, et al.