

Candidate for Executive Committee

Marcus Riedel
University of Minnesota, Minneapolis, MN USA

BIOGRAPHY

Academic Background:

Ph.D., Caltech, 2004, Electrical Engineering.

Professional Experience:

Assistant Professor, University of Minnesota, Minneapolis, MN, 2006 – 2009;
Postdoctoral Fellow, Caltech, Pasadena, CA, 2005;
Research Assistant, Caltech, Pasadena, CA, 2003 – 2004.

Professional Interest:

Bio-Design Automation; Synthetic Biology; Logic Synthesis; Verification; Emerging Technologies.

ACM Activities:

Technical Program Committee Chair of IWLS 2009, SIGDA, 2009;
General Chair of IWLS 2008, SIGDA, 2008;
Co-Chair, SIGDA Technical Committee on RTL and Logic Synthesis, SIGDA, 2006 – 2009;
Associate Editor, SIGDA Newsletter, SIGDA, 2006 – 2009.

Awards Received:

Charles Wilts Award for Best EE Doctoral Dissertation at Caltech, 2004; Best Paper Award, Design Automation Conference, 2003.

STATEMENT

As a member of the SIGDA Executive Committee, I will strive to invigorate research in fields such as logic synthesis, optimization and verification, and to promote new directions for these fields. I am particularly interested in the application of expertise from logic synthesis to emerging areas such as nanotechnology and synthetic biology. I will lobby for increased funding, visibility and coverage of papers related to logic synthesis in major CAD conferences, and provide a link between SIGDA, academia and industry.