SGB Position: SGB Council Representative

Norman P. Jouppi
Fellow and Director, Exascale Computing Lab, HP Labs, Palo Alto, California.

BIOGRAPHY:

Ph.D. in Electrical Engineering from Stanford University in 1984, M.S.E.E. from Northwestern University in 1980, and B.S.E.E. from Northwestern University in 1979.

After graduation he joined DEC's Western Research Lab, which was acquired by Compaq and then Hewlett Packard. From 1984 through 1996 he was also a consulting assistant/associate professor in the department of Electrical Engineering at Stanford University where he taught classes in VLSI, circuits, and computer architecture. He is currently the director of the Exascale Computing Lab at HP Labs.

He is known for his work in memory systems and developing widely used prefetching techniques. He started his career as one of the principal architects and designers of the Stanford MIPS microprocessor and as a developer of techniques for CMOS VLSI timing verification. Later, at DEC he was the principal architect and lead designer of the MultiTitan and BIPS microprocessors. He has also contributed to the architecture and implementation of advanced graphics accelerators, and extensively researched audio, video and physical telepresence. He holds more than 35 U.S. patents and has published over 100 technical papers.

SGB ACM Council Representative (2006-present), SGB EC Vice Chair for Operations (2006-2007), SGB EC Member at Large and conference advisor (2005-2006), Past Chair of SIGARCH (2007-present), Chair of SIGARCH (2003-2007), Vice Chair of SIGARCH (1999-2003), Member of the SIGARCH Board (1993-1999). ISCA PC Chair 1996. ACM CRA board representative (2008-present). CACM research highlights editorial board (2008-present). IEEE Computer Architecture Letters editorial board (2001-present). IEEE TCCA advisory board (2002-2005).

Fellow of the ACM and IEEE. Member of ACM SIGARCH, SIGMICRO, SIGGRAPH, SIGMM, and SIGMETRICS.

Compaq 2002 Key Patent award. 2005 ISCA Influential Paper award and two SIGGRAPH/Eurographics Workshop on Graphics Hardware best paper awards.

STATEMENT FOR PUBLICATION:

I would welcome the opportunity to continue to actively represent the interests of the SIGs on the ACM Council. While serving in this capacity over the last three years I've been able to contribute to a number of efforts, such as the Council's membership strategy and the SGB's new SIG development task forces. I'm also serving in a number of Council capacities, including one of ACM's two representatives to the Computing Research Association Board. I'm a strong believer in the importance of the SIGs to the ACM, and would like to continue to serve them as an SGB ACM Council Representative.

I have a very broad set of interests, and am currently an active member of five different SIGs (SIGARCH, SIGMICRO, SIGGRAPH, SIGMM, and SIGMETRICS). I've worked in both industry and academia. As part of this diverse experience I've developed an appreciation of the range of communities served by the SIGs, and I believe I represent them well on the ACM Council.

As SGB ACM Council Representative I would continue to diligently work to represent the needs and interests of the SIGs on the ACM Council. I would also strive to facilitate the operation of the SIGs. Broadening the membership and participation of international, industrial, and underrepresented groups in the ACM and its SIGs would remain one of my priorities. I'm currently serving as a member of the CACM research highlights editorial board – working with the SIGs to obtain paper nominations is a key part of the paper solicitation process. I am looking forward to continuing to attend SGB meetings to hear your concerns and suggestions.

I've enjoyed my service as a SGB ACM Council Representative, and my interactions with the talented ACM staff and SGB volunteers. I would like to contribute more to the ACM, its SIGs, and its members. I am excited by this opportunity and will continue to serve at the best of my ability if elected.