Candidate for Chair

Babak Falsafi
EcoCloud/EPFL, Lausanne, Switzerland

BIOGRAPHY

Academic Background:
Ph.D., University of Wisconsin, 1998, Computer Science.

Professional Experience:
Director, EcoCloud/EPFL, Lausanne, Switzerland, 2011 – Present;
Professor, EPFL, Lausanne, Switzerland, 2008 – Present;
Professor, Carnegie Mellon, Pittsburgh, PA, USA, 2001 – 2008.

Professional Interest:
Near-Memory Processing; Network-Centric Server Architecture; Machine Learning Systems.

ACM Activities:
Board of Directors, SIGARCH, 2015 – Present;
Program Chair, ISCA, SIGARCH, 2018.

Awards Received:
ACM Fellow, 2015;
Best Paper Award, ASPLOS, 2012;
IEEE Fellow, 2011;
Sloan Fellow, 2004.

STATEMENT

I am honored to have served among a dedicated SIGARCH team who helped usher in a new set of initiatives to establish platforms for technical exchange, talent development and recognition, and outreach not only to neighboring technical communities but the broader internet audience. Specifically, I created and led a SIGARCH communication team to launch Computer Architecture Today, social media, a YouTube! Channel and a new website and newsletter. Our SIGARCH initiatives have also included addressing key challenges facing our day-to-day affairs including review processes and inclusivity, with percolated positive impact on other parts of ACM. Alas, we have only touched the surface on these grand challenges given four years in office. I am fully committed to lead a team and work with our colleagues both in ACM and technical in committees in IEEE to push the community forward along these grand SIGARCH missions.

Candidate for Chair

Gabriel H. Loh
Advanced Micro Devices, Inc., Bellevue, WA, USA

BIOGRAPHY

Academic Background:
Ph.D., Yale University, 2002, Computer Science.

Professional Experience:
Research Fellow, Advanced Micro Devices, Inc., Bellevue, WA, USA, 2010 – 2018;
Associate Professor, Georgia Institute of Technology, Atlanta, GA, USA, 2003 – 2011.

Professional Interest:
Computer Architecture.

ACM Activities:
Committee Member, SIGARCH Reviewing Reviewing (R2) Committee, 2016 – 2018;
General Chair, ISCA 2016, SIGARCH, 2015 – 2016.

STATEMENT

I am running for Chair because this is a community that I have been a part of for some time, and I feel that it is good for us all to give back and serve our technical community. SIGARCH is a vibrant community, and I would like to work with our community to continue it's impact and contributions to industry and society.

Candidate for Vice-Chair

Natalie Enright Jerger
University of Toronto, Ontario, Canada

BIOGRAPHY

Academic Background:
Ph.D., University of Wisconsin-Madison, 2008, Electrical Engineering.

Professional Experience:
Professor, University of Toronto, ON, Canada, 2017 – Present;
Associate Professor, University of Toronto, ON, Canada, 2014 – 2017;
Visiting Researcher, AMD, Bellevue, WA, USA, 2015 – 2016.

Professional Interest:
Computer Architecture; Interconnection Networks; Approximate Computing.

ACM Activities:
Board of Directors, SIGARCH, 2015 – Present;
Vice-Chair, SIGMICRO, 2017 – Present;
Associate Editor, ACM Transactions on Architecture and Code Optimization, 2013 – 2018.

Membership and Offices in Related Organizations:
Executive Committee, IEEE CS TCCA, 2015 – 2018;
Associate Editor, Computer Architecture Letters, IEEE, 2014 – Present;
Associate Editor, Transactions on Computers, IEEE, 2014 – 2018.

Awards Received:
ACM Distinguished Member, 2018;
Borg Early Career Award, 2015;
Alfred P. Sloan Fellow, 2015;
Ontario Professor Engineers Young Engineer, 2014.

STATEMENT

I ran for the SIGARCH Executive Committee in 2015 on a platform of creating new awards. In that capacity, I spearheaded the creation of the ACM SIGARCH/IEEE CS TCCA Outstanding Dissertation Award. The inaugural award was given at ISCA 2018. As a member of the executive committee of TCCA, I led the creation of the HPCA Test of Time Award with the inaugural award given in 2018. As Vice-Chair, I will look for additional ways to celebrate and recognize the accomplishments of our members, especially collaborative and team efforts.

The other area I would like to focus on is equity, diversity and inclusion. For the last 2 years, I've served as chair of the revamped Women in Computer Architecture Subcommittee of SIGARCH. We have created a vibrant online mentoring community that has increased the sense of belonging and support among our members. I will continue to work to make our community and conferences more diverse and inclusive and drive mentoring efforts. Finally, I was involved in a multi-year effort to recommend changes to our conference review processes. This work needs to continue and recommendations need to be discussed and implemented with broad support of the community.

Candidate for Vice-Chair

Mattan Erez
The University of Texas at Austin, TX, USA

BIOGRAPHY

Academic Background:
Ph.D., Stanford, 2007, Electrical Engineering.

Professional Experience:
Professor, The University of Texas at Austin, TX, USA, 2018 – 2019;
Associate Professor, The University of Texas at Austin, TX, USA, 2012 – 2018;
Assistant Professor, The University of Texas at Austin, TX, USA, 2007 – 2012.

Professional Interest:
Computer Architecture; Memory Systems; High Performance Computing; Resilience; Machine Learning.

ACM Activities:
Member: ACM and SIGARCH, 2007 – Present.

STATEMENT

In addition to supporting current SIGARCH initiatives, I would like to advance and explore ideas that will improve our paper review process and maintain the anonymity of authors and reviewers. I believe this must be done and will require some cultural shifts. While much should be left to the discretion of PC Chairs, aspects of review quality and reviewer effort span conferences. I hope to promote mechanisms that will increase accountability (of authors and reviewers) and ultimately improve the quality of both reviews and papers. I also believe that our community's culture of diverse conferences and double-blind reviewing is important. For example, I have already started working on an idea to preserve double-blind reviewing in the arXiv age. While changes may be tough, I will approach any new ideas with humility and an open mind.

Candidate for Secretary-Treasurer

Rajeev Balasubramonian
University of Utah, Salt Lake City, UT, USA

BIOGRAPHY

Academic Background:
Ph.D., University of Rochester, 2003, Computer Science.

Professional Experience:
Professor, University of Utah, Salt Lake City, UT, USA, 2015 – Present;
Associate Professor, University of Utah, Salt Lake City, UT, USA, 2009 – 2015;
Assistant Professor, University of Utah, Salt Lake City, UT, USA, 2003 – 2009.

Professional Interest:
Memory Security and Privacy; Systems for Genomic Analysis; Accelerators for Machine Learning; Memory System Efficiency; Large Cache Hierarchies.

ACM Activities:
ASPLOS: General Chair and Steering Committee, SIGARCH, 2014 – 2016;
ISCA: Finance Chair (2011 and 2016), Student Travel (2013), SIGARCH, 2011 – 2016;
PACT: Finance Chair (2017), Student Research Competition (2012), SIGARCH, 2012 – 2017;
Co-organizer of 6 workshops/tutorials affiliated with ISCA, SIGARCH, 2008 – 2019.

Membership and Offices in Related Organizations:
Co-Program Chair, IEEE HPCA, 2018 – 2019;
ISPASS Steering Committee Chair, IEEE, 2017 – 2019;
Finance Chair: HPCA, IISWC, IEEE, 2012 – 2013.

Awards Received:
Intel Outstanding Research Award, 2017;
IBM Faculty Partnership Award, 2013;
NSF CAREER Award, 2006;
Outstanding Teaching Award, SoC, University of Utah, 2005.

STATEMENT

I have been an energetic and engaged volunteer in architecture circles for many years, often dealing with industry/federal sponsorships for conferences. More recently, I have made a conscious commitment to lead more efforts to improve the system:

1. Inclusion: Led the revision of the ISPASS charter to formally consider diversity and inclusion.

2. Reviewing: (with my HPCA'19 program co-chair) introduced mechanisms to reduce reviewer effort and improve reviewer accountability.

3. Growing the pipeline: (with colleagues at U. Utah) helping expand robotics and coding camp offerings to students in Title I schools in Utah (schools catering to low-income populations).

In collaboration with SIGARCH leadership, I will continue to push for better practices for reviewing and inclusion. I will leverage my conference fundraising experience to coordinate best practices among finance chairs, by sharing databases and jointly requesting funds for SIGARCH conferences. I will work to enhance past efforts at growing membership and digital library engagement. I also believe SIGARCH can be a leader within ACM by creating pilot programs for outreach: I will work to secure industry funding for small grants/scholarships that architecture researchers can use for outreach.

Candidate for Secretary-Treasurer

Karin Strauss
Microsoft, Redmond, WA, USA

BIOGRAPHY

Academic Background:
Ph.D., University of Illinois, 2007, Computer Science/Computer Architecture.

Professional Experience:
Principal Researcher, Microsoft, Redmond, WA, USA, 2009 – 2019;
Researcher, AMD, Bellevue, WA, USA, 2007 – 2009;
Research Assistant, University of Illinois, Urbana, IL, USA, 2002 – 2007.

Professional Interest:
DNA data storage and computing; Emerging memory technologies; Machine learning accelerators; Cloud computing; Cache coherence and hardware support for debugging.

ACM Activities:
EC Director, SIGARCH, 2015 – Present;
Program co-chair for ASPLOS 2020, SIGARCH, 2018 – Present;
Co-organizer of Workshop on Trends in Machine Learning at ISCA17, SIGARCH, 2016 – 2017;
ACM Senior Member, 2014.

Membership and Offices in Related Organizations:
Selection Committee Co-chair of IEEE Micro Magazine Top Picks, IEEE, 2014 – 2015;
Finance Chair, ACM SIGPLAN PPoPP, 2010 – 2011;
Local Arrangements Chair for IISWC, IEEE, 2007 – 2008.

Awards Received:
PPoPP Keynote Speaker, 2019;
Fast Company Magazine's 100 Most Creative People in Business, 2016;
Elevated to IEEE Senior Member, 2014.

STATEMENT

For the past 4 years, I've been committed to the Computer Architecture community though service in the SIGARCH Executive Committee. This has given me the opportunity to monitor the health of our conferences though the conference approval process, launch an initiative to video-record talks during our conferences that culminated in the SIGARCH YouTube Channel, and co-launch an official process to select and incite Visioning Workshops in Computer Architecture. This resulted in a Trends in Machine Learning Workshop with over 150 attendees at ISCA 2017 and two new

workshops coming up in 2019 and 2020. If elected, I plan to continue our effort on conference health, including a review of its history, connection to our members, and adherence to ACM's Code of Ethics and Anti-Harassment Policy. I will also continue our efforts on fostering future-focused discussions through the Visioning Workshops Committee and other related initiatives.

Candidate for Board of Directors

Yungang Bao
Chinese Academy of Sciences, Beijing, China

BIOGRAPHY

Academic Background:
Ph.D., Institute of Computing Technology, Chinese Academy of Sciences, 2008, Computer Science.

Professional Experience:
Associate/Full Professor, Institute of Computing Technology, Chinese Academy of Sciences, Beijing, China, 2012 – Present;
Postdoc, Princeton University, Princeton, NJ, USA, 2010 – 2012;
Assistant Professor, Institute of Computing Technology, Chinese Academy of Sciences, Beijing, China, 2008 – 2010.

Professional Interest:
Computer Architecture; Operating System.

ACM Activities:
Vice Chair, ACM China, 2018 – Present;
Vice-Chair, ACM SIGOPS ChinaSys Chapter, 2017 – Present.

Membership and Offices in Related Organizations:
Council Member, China Computer Federation (CCF), 2017 – Present.

Awards Received:
CCF-Intel Young Faculty Researcher Program Award, 2013.

STATEMENT

Computer architecture has been drawing more and more attention in China over the past decade. Take SIGARCH's flagship conference ISCA as an example, Chinese scholars have published 20+ ISCA papers since 2008, largely booming compared with the number before 2007 (only five). Moreover, Chinese scholars have been doing world-class research. For example, they have collaborated with INRIA, ICT's DinaNao family work won the best paper awards at ASPLOS and MICRO and was selected to CACM research highlights.

However, the Chinese computer architecture community is still underrepresented in ACM SIGARCH. Therefore, I decided to run for director of ACM SIGARCH. If I am elected, I will focus my efforts on promoting the impact of SIGARCH in China as well as in the Far East region:

- I will launch SIGARCH China chapter to help more Chinese scholars to join SIGARCH.

- I will organize research groups to write comments on the most ISCA Influence papers in Chinese/Japanese/Korean and translate stories of Eckert-Mauchly awardees so that the impact of SIGARCH can reach more Asian communities.

- I will help establish connections between international scholars and Chinese Companies, through visiting, collaboration and sponsorship etc.

Candidate for Board of Directors

Abhishek Bhattacharjee
Yale University, New Haven, CT, USA

BIOGRAPHY

Academic Background:
Ph.D., Princeton University, 2010, Electrical Engineering.

Professional Experience:
Associate Professor of Computer Science, Yale University, New Haven, CT, USA, 2019 – Present;
Associate Professor of Computer Science, Rutgers University, New Brunswick, NJ, USA, 2016 – 2018;
Assistant Professor of Computer Science, Rutgers University, New Brunswick, NJ, USA, 2010 – 2016.

Professional Interest:
Computer Architecture; Operating Systems; Brain Sciences.

ACM Activities:
Member: ACM and SIGARCH, 2010 – Present.

Membership and Offices in Related Organizations:
Member, IEEE, 2010 – Present.

Awards Received:
Rutgers Chancellor's Award for Faculty Excellence in Research, 2017;
Princeton Neuroscience Institute CV Starr Fellowship, 2017;
National Science Foundation CAREER Award, 2013.

STATEMENT

If elected, I would explore ways to foster ties with scientists working in areas like medicine, Climate change, etc. The goal would be to help computer architects engage with experts from these research streams with a view to opening new avenues for research funding and non-traditional job opportunities for graduate students. (Specific initiatives could involve programs that fund our graduate students to attend conferences in emerging areas like brain-machine interface design, host engineers from BrainGate/Neuralink to describe their work at our conferences, etc.). I will also explore ways to increase involvement of undergraduates at our conferences. One way of doing this may be to mirror initiatives like the Ph.D. dissertation award but for undergraduates performing research in computer architecture. My goal would be to use such schemes to also encourage participation from undergraduates with backgrounds under-represented in STEM.

Candidate for Board of Directors

Reetuparna Das
University of Michigan, Ann Arbor, MI, USA

BIOGRAPHY

Academic Background:
Ph.D., Pennsylvania State University, 2010, Computer Science and Engineering.

Professional Experience:
Assistant Professor, University of Michigan, Ann Arbor, MI, USA, 2016 – Present;
CSO & Co-founder, Sequal Inc, Ann Arbor, MI, USA, 2018 – Present;
Assistant Research Scientist, University of Michigan, Ann Arbor, MI, USA, 2011 – 2015.

Professional Interest:
In-Memory Computing; Hardware Acceleration for Machine Learning;
Custom Computing for Genomics and Precision Health; Heterogenous Core Architectures;
Network-on-Chip.

ACM Activities:
Program co-chair for MICRO 2019, SIGMICRO, 2019;
Program committee member for ISCA, ASPLOS, MICRO, PACT, 2015 – Present;
WiCArch Organizing Committee member, SIGARCH, 2017 – Present;
Publication chair for ISCA 2018, SIGARCH.

Membership and Offices in Related Organizations:
SIGARCH Video Officer, 2018 – Present;
SIGARCH Computer Architecture Today (CAT) regular blogger, 2017 – Present.

Awards Received:
Sloan Research Fellowship, 2019;
CRA-W Borg Early Career Award, 2018;
NSF CAREER Award, 2017;
MICRO and ISCA Hall of Fame, 2018.

STATEMENT

I am honored to run for the Executive Committee. If elected, I will work to ensure that SIGARCH remains a vibrant, visible, and inclusive organization. I plan to focus on several initiatives, such as:

(1) Improving Diversity. Recently, we have made significant progress towards improving diversity. I have played my part as a member of the WiCArch organizing committee.

For instance, I started an initiative to improve the visibility of women faculty candidates. I plan to invest efforts towards improving the pipeline. For instance, we can provide support to emerging undergraduate scholars for attending conferences, and host outreach activities (hackathons, demos, and activities geared towards high-school students) on our pre-conference days.

(2) Scaling our conferences. It is the golden age for computer architecture. I am excited to be a part of it. The number of accepted papers have doubled and the number of conference attendees has tripled over the last decade. Scaling architecture conferences without losing quality is an important problem facing us. We can learn from the experience of our sister communities that have addressed this problem (e.g. NeurIPS). I would like to engage and seek input from the broader SIGARCH community to find creative solutions.

Candidate for Board of Directors

Joel Emer
Nvidia, Westford, MA, USA

BIOGRAPHY

Academic Background:
Ph.D., University of Illinois, 1979, Electrical Engineering.

Professional Experience:
Senior Distinguished Research Scientist, Nvidia, Westford, MA, USA, 2014 – Present;
Professor of the Practice, MIT, Cambridge, MA, USA, 2010 – Present;
Intel Fellow, Intel, Hudson, MA, USA, 2001 – 2014.

Professional Interest:
Spatial architectures; Architectures for Deep Learning; Parallel Processor Organization; Memory Hierarchy Design; Performance Modeling.

ACM Activities:
Director, SIGARCH, 2015 – Present;
MICRO Program Chair, SIGMICRO, 2017;
Director, SIGARCH, 2003 – 2007;
ISCA Program Chair, SIGARCH, 2000.

Membership and Offices in Related Organizations:
CRA Board Member, 2015 – 2018.

Awards Received:
Eckert-Mauchly Award, 2009;
ACM Fellow, 2005;
IEEE Fellow, 2004;
ISCA Test of Time Award, 2011.

STATEMENT

I view computer architecture as continuing to be a vibrant discipline, and I believe that SIGARCH is an ideal forum to foster its evolution. In my opinion, SIGARCH serves a variety of functions, including:

- fostering high-quality research by assuring fair review and dissemination of new research;
- stimulating discourse on important research and professional topics;

- facilitating the development of new research directions;
- recognizing and rewarding the accomplishments of individuals; and
- in encouraging the participation of young people, women and minorities.

Over the years, I have served SIGARCH and the ACM in a variety of ways that I feel have aided in those objectives, including service as an officer and board member and as program and general chair of conferences. Recently this has included conceiving and running the “Meet a Senior Architect” program, co-chairing the visioning workshop committee and membership in the CARES committee. I would be pleased to have the opportunity continue these activities and bring my many years of experience (academic and industrial) in the community and perspective to the SIGARCH board.

Candidate for Board of Directors

Boris Grot
University of Edinburgh, United Kingdom

BIOGRAPHY

Academic Background:
Ph.D., University of Texas at Austin, 2011, Computer Science.

Professional Experience:
Associate Professor, University of Edinburgh, United Kingdom, 2018 – Present;
Assistant Professor, University of Edinburgh, United Kingdom, 2014 – 2018;
Post-doctoral researcher, EPFL, Lausanne, Switzerland, 2011 – 2013.

Professional Interest:
Computer architecture and microarchitecture; Memory systems; Interconnection networks; Servers and datacenters.

ACM Activities:
Content Editor, ACM SIGARCH, 2014 – Present;
Program Committee, ASPLOS, SIGARCH, 2019;
Program Committee, ISCA, SIGARCH, 2018;
Program Committee, ASPLOS, SIGARCH, 2018.

Awards Received:
HPCA Best Paper nominee, 2019;
MICRO Hall of Fame inductee, 2016;
MICRO Best Paper Runner-up, 2013;
IEEE Micro Top Pick, 2012.

STATEMENT

In my current role as the SIGARCH Content Editor, I launched iscaconf.org and co-managed a complete overhaul of sigarch.org. These initiatives show my commitment to SIGARCH and to its members. I am now running for a place on the SIGARCH Board of Directors because I see a critical need to address pressing shortcomings in the practices of paper submission, assignment and reviewing in our flagship conferences. I firmly believe that many of the deficiencies associated with the current practices are rooted in the overload experienced by the program chair and the program committee due to high submission volumes. The overload leads to numerous problems including difficulties in sourcing experts, low quality reviews, late reviews and noisy decisions at paper selection time. The solution toward which I commit to work, if elected, is to spread the assignment, reviewing and decision-making load over multiple deadlines. I recognize that there is no perfect system, but I also believe that we must take bold, forward-looking steps to allow our reviewing practices to accommodate continuing growth in submission counts and increased diversity of research topics in our vibrant field.

Candidate for Board of Directors

Martha Kim
Columbia University, New York, NY, USA

BIOGRAPHY

Academic Background:
Ph.D., University of Washington, 2008, Computer Science and Engineering.

Professional Experience:
Associate Professor, Columbia University, New York, NY, USA, 2015 – Present;
Assistant Professor, Columbia University, New York, NY, USA, 2009 – 2015.

Professional Interest:
Computer Architecture; Low Power Design; Compilation.

ACM Activities:
Member and Chair, Doctoral Dissertation Award Committee, 2016 – Present;
Associate Editor, ACM TACO, 2017 – Present;
Finance Co-chair, IEEE/ACM MICRO 2018, SIGMICRO;
Tutorial Chair, IEEE/ACM ISCA 2016, SIGARCH, 2015 – 2016.

Membership and Offices in Related Organizations:
Member and Chair, IEEE TCCA Young Architect Award Committee, 2017 – Present;
Associate Editor, Computer Architecture Letters, 2016 – Present;
Member, IEEE TCCA Executive Committee, 2017 – 2018.

Awards Received:
Borg Early Career Award, CRA-W, 2016;
IEEE Micro Top Picks in Computer Architecture, 2015;
IEEE Micro Top Picks in Computer Architecture, 2014.

STATEMENT

I am honored to run for a seat on the SIGARCH Executive Committee. I have been active in the computer architecture community for ten years and am a lifetime member of the ACM. If elected, I will help SIGARCH channel the energy of the computer architecture community into traditions and new programs. The SIGARCH blog has become a virtual watering hole, where ideas of all sorts are shared and discussed, and, on several occasions beyond the local computer architecture community. Our organizing bodies can cultivate and steer this appetite for improvement, particularly in receiving and filtering feedback. This information can be used

in many constructive ways. For example, it can be used to refine living documents of best practices. Or, questions can seed a blog post explaining the rationale for why things are done in a particular way, can improve transparency and help surface better approaches. With careful stewardship, the computer architecture community can become a beacon in research, education, and culture.

Candidate for Board of Directors

José F. Martínez
Cornell University, Ithaca, NY, USA

BIOGRAPHY

Academic Background:
Ph.D., University of Illinois at Urbana-Champaign, 2002, Computer Science.

Professional Experience:
Professor, Cornell University, Ithaca, NY, USA, 2002 – Present.

Professional Interest:
Computer Architecture.

ACM Activities:
General Chair, ISCA, SIGARCH, 2020;
Associate Editor, ACM TACO, 2009 – 2015;
Program Co-chair, MICRO, SIGMICRO, 2009.

Membership and Offices in Related Organizations:
Chair, Transactions Operating Committee, IEEE Computer Society, 2017;
Editor in Chief, Computer Architecture Letters, IEEE Computer Society, 2013 – 2016;
Program Chair, HPCA, IEEE Computer Society, 2016.

Awards Received:
HPCA Best Paper Nomination, 2015;
IEEE MICRO Top Picks, 2007;
MICRO Best Paper Nomination, 2006;
HPCA Best Paper Award, 2005.

STATEMENT

There are arguably three top-tier "core" computer architecture conferences: HPCA, ISCA, and MICRO. While it is important to recognize that there are differences in their history and character, these three conferences do share a large and vibrant computer architecture research community. Having served on the journal side of both the ACM and the IEEE Computer Society for about 10 years combined, I believe the time is right to create a new, shared ACM/IEEE journal-first model tied to these three conferences. This shared journal would constitute a common archival forum for our research community worldwide, in harmony with these conferences existing character, organization, and calendar.

Should I be fortunate to be elected to SIGARCH's Board of Directors, I would work toward joining forces with our sister organizations within ACM and the IEEE Computer Society in pursuit of this goal, enabled by inclusive, whole-community discussion.

Candidate for Board of Directors

Andreas Moshovos
University of Toronto, Ontario, Canada

BIOGRAPHY

Academic Background:
Ph.D., University of Wisconsin-Madison, 1998, Computer Sciences.

Professional Experience:
Assistant and Full Professor, University of Toronto, Ontario, Canada, 2000 – 2019;
Assistant and Professor, Northwestern University, Evanston, IL, USA, 1999 – 2000.

Professional Interest:
Computing Hardware; Computer Architecture.

ACM Activities:
Member, SIGARCH, 1997 – Present;
Member, ACM, 1993 – Present.

Awards Received:
SIGARCH Maurice-Wilkes Award, 2010.

STATEMENT

Our reviewing system is being stressed to its limits and needs to be improved to better serve all of us. This is not a challenge unique to our community. I do not have a magical solution either. What I hope you will allow me to work on is developing a process that will enable us *all* to collaboratively explore options and to continuously evolve. Currently, we all learn how to judge the work of others by trial and error and by example. Information and experience are passed on anecdotally or witnessed during the review process. Some is good, some not so much, some can be controversial and a lot of it is forgotten. I think this process is failing a lot of us. Ultimately, we ought to develop a living set of guidelines that all can refer to and change as needed based on experience and outside the context of a PC meeting. One way to achieve that is by establishing a platform for exchanging experiences/ideas on how to move forward and a process for distilling these into non-binding guidelines about process, standards, etc. My goal will be to work with anyone interested in exploring such options.

Candidate for Board of Directors

Tim Sherwood
University of California, Santa Barbara, CA, USA

BIOGRAPHY

Academic Background:
Ph.D., University of California, San Diego, 2003, Computer Science and Engineering.

Professional Experience:
Associate Vice Chancellor for Research, University of California, Santa Barbara, CA, USA, 2016 – Present;
Co-Founder, Tortuga Logic, Santa Barbara, CA, USA, 2013 – Present;
Assistant/Associate/Full Professor of Computer Science, University of California, Santa Barbara, CA, USA, 2003 – Present.

Professional Interest:
Computer Architecture; Hardware Security; Neuromorphic Architectures; Formal Methods in Hardware/Software Development; Frontiers of Computing.

ACM Activities:
SIGARCH/TCCA Outstanding Dissertation Award Committee, SIGARCH, 2018 – Present;
ACM Student Chapter Liaison, 2003 – 2009.

Awards Received:
SIGARCH Maurice Wilkes Award, 2016;
ASPLOS Most Influential Paper Award, 2017;
UCSB Academic Senate Distinguished Teaching Award, 2012;
UCSB Outstanding Organization Advisor Award (for ACM), 2009.

STATEMENT

As the contact point between the colliding worlds of hardware and software, the future of computing will be written by those that understand computer architecture. That future deserves to be written by a truly diverse, well-trained, enthusiastic, and engaged community of computer architecture practitioners and scholars. SIGARCH is an amazing community that has been thriving under its recent leadership and has already implemented many important changes. Three immediate challenges I see for SIGARCH are:

1) ensuring that those positive changes already made continue uninterrupted in perpetuity to become "the new normal",

2) that we find ways to scale our community to meet demand without sacrificing the aspects of our culture which make it so great, and

3) that we help a larger and more diverse pipeline of young scientists and engineers access and contribute to this important and growing discipline. I am eager to serve our community in meeting these and other challenges.

Candidate for Board of Directors

Thomas F. Wenisch
University of Michigan, Ann Arbor, MI, USA

BIOGRAPHY

Academic Background:
Ph.D., Carnegie Mellon University, 2007, Electrical & Computer Engineering.

Professional Experience:
Associate Professor & Associate Chair, Computer Science & Engineering, University of Michigan, Ann Arbor, MI, USA, 2007 – Present;
Visiting Scientist, Google, Madison, WI, USA, 2013 – Present.

Professional Interest:
Computer Architecture; Server & Data Center Systems; Memory Persistency;
Performance Evaluation Methodology; Architectural Support for Medical Imaging.

ACM Activities:
ISLPED: PC Co-Chair (2017), General Co-Chair (2018), SIGDA, 2017 – 2018;
SIGARCH/TCCA Reviewing Committee, SIGARCH, 2017 – 2018;
Associate Editor, ACM TACO, SIGARCH, SIGMICRO, SIGPLAN, 2017 – 2019;
Program Co-Chair, MICRO, SIGMICRO, 2014.

Membership and Offices in Related Organizations:
PC Chair (2018), General Chair (2019) ISPASS, IEEE TCCA/TCuARCH/TCI, 2017 – 2019;
Executive Committee Member, IEEE TCCA, 2015 – 2018.

Awards Received:
7 IEEE Micro Top Picks, 1 Honorable Mention 2009-2019;
UM CSE Outstanding Achievement Award, 2016;
University of Michigan Henry Russel Award, 2013;
NSF CAREER Award, 2009.

STATEMENT

With the end of Moore's law and Dennard scaling, the emergence of threats like Spectre and Meltdown, and the critical need for efficient computational substrates for machine learning and AI, computer architecture research has never been more critical or relevant. Because of our renewed importance, the computer architecture community--and our flagship conferences--continue to grow. While this growth is an outstanding boon for the health of our discipline, it nevertheless creates enormous challenges in the effective dissemination of our research, management of

our review processes, stewardship of the career progression of our junior colleagues, and fostering of a welcoming and inclusive atmosphere at our professional meetings. Furthermore, given the enormous growth in computer science as a whole, and the lack of commensurate growth in U.S. federal and international research support, it is critical that SIGARCH and our other leadership bodies continually articulate the importance of our work and lobby for expanded research support. As a SIGARCH director, I am committed to work on all of these fronts to help steer our community to play a leadership role across computer science.