

Candidate for Chair

Frank Pfenning
Carnegie Mellon University, Pittsburgh, PA, USA

BIOGRAPHY

Academic Background:

Ph.D., Carnegie Mellon University, 1987, Mathematics.

Professional Experience:

Professor, Computer Science Department, Carnegie Mellon University, Pittsburgh, PA, USA,
2002 – Present;

Head, Computer Science Department, Carnegie Mellon University, Pittsburgh, PA, USA,
2013 – 2018;

Associate Dean, School of Computer Science, Carnegie Mellon University, Pittsburgh, PA, USA,
2009 – 2010.

Professional Interest:

Type theory; Automated deduction; Logical frameworks; Computer security;
Computer science education.

ACM Activities:

Member of LICS Steering Committee, SIGLOG, 2008-2010 & 2018 – Present;

Program Chair, LICS 2008, ACM/IEEE (now: SIGLOG/IEEE);

General Chair, PPDP 2002, ACM SIGPLAN;

Program Chair, PPDP 2000, ACM SIGPLAN.

Membership and Offices in Related Organizations:

Member of FoSSaCS Steering Committee, ETAPS, 2018 – Present;

President, CADE, 2008 – 2009;

Trustee, CADE, 1998 – 2004.

Awards Received:

LICS Test-of-Time Award (with I. Cervesato) for LICS'96 paper, 2016;

ACM Fellow, 2015;

Alexander-von-Humboldt Fellowship, 1996;

Fulbright Fellowship, 1980.

STATEMENT

I have always considered 'Logic in Computer Science' my primary area of research, pervading my work on logical frameworks, type theory, automated deduction, logic programming, computer

security, and computer science education. I am therefore pleased to offer my services to SIGLOG as a way to give back to the community that has been instrumental to my academic career (including 11 papers published at LICS, 4 in ToCL, and serving on the LICS PC 8 times). Already, SIGLOG has made a significant difference through sponsorship of conferences, publications, and awards, and there are opportunities for growth in these activities. For example, additional awards such as a dissertation award and a service award would recognize leadership efforts and benefit young researchers. My further priorities include mentoring students and faculty (perhaps along the lines of successful Programming Languages Mentoring Workshop (PLMW) sponsored by SIGPLAN), supporting and increasing diversity (including, for example, Women in Logic), providing educational resources, and evaluating conditions of publication in support of open access.

Candidate for Chair

Joost-Pieter Katoen
RWTH Aachen University, Germany

BIOGRAPHY

Academic Background:

Ph.D., University of Twente (NL), 1996, Computer Science.

Professional Experience:

Full Professor, RWTH Aachen University, Germany, 2004 – Present;
Full Professor (part-time), University of Twente, Enschede, The Netherlands, 2009 – Present;
Associate Professor, University of Twente, Enschede, The Netherlands, 1999 – 2009.

Professional Interest:

Model checking; Program verification; Probabilistic computation; Concurrency theory;
Formal semantics.

ACM Activities:

Invited Tutorial Speaker, IEEE/ACM LICS, 2016;
Organiser and Initiator, EATCS/ETAPS/SIGLOG/SIGPLAN FoPPS Summer School, 2017;
Invited Tutorial Speaker, IEEE/ACM ASE, 2012;
Guest Editor, ACM SIGMETRICS Performance Evaluation, 2005.

Membership and Offices in Related Organizations:

ETAPS Steering Committee Chair, European Joint Conferences on Theory and Practice of Software, 2013 – 2019;
Member IFIP WG on Formal Description of Programming Concepts, IFIP, 2010 – Present;
Member, Academia Europaea, 2013.

Awards Received:

ERC Advanced Research Grant, 2018;
Honorary Doctorate University of Aalborg, Denmark, 2017;
Distinguished Professorship, RWTH Aachen University, 2013.

STATEMENT

Whereas SIGLOG is currently strongly focused on logics, my ambition is to put emphasis on computer-aided verification too. Verification techniques such as model checking heavily rely on logics for specifying formal requirements. Logics are pivotal for decision procedures in program verification as well. SIGLOG could strengthen its ties to a larger variety of conferences (such as CSL, MFPS, MFCS, CAV, TACAS) and continue/extend its successful collaboration with related associations such as EATCS, ETAPS, and ACM SIGPLAN to organise joint activities such as summer schools for young researchers.

Candidate for Chair

Simona Ronchi Della Rocca
University of Torino, Italy

BIOGRAPHY

Academic Background:

Degree in Physics, University of Torino, 1969, Nuclear Physics.

Professional Experience:

Professor Emeritus, University of Torino, Italy, 2018 – Present;
Full professor, University of Torino, Italy, 1987 – 2016;
Associated Professor, University of Torino, Italy, 1983 – 1987.

Professional Interest:

Logic and Type Theory; Semantics of Programming Languages;
Implicit Computational Complexity.

ACM Activities:

Member, ACM & SIGLOG, 2018 – Present.

Membership and Offices in Related Organizations:

Vice-President, EACSL, 2012 – Present.

STATEMENT

Three main problems:

Teaching Logic. Many universities have computer science undergraduate curricula without courses in logic. Logic cannot be taught only at the graduate level: logic is a way of thinking, and needs to be learned at the beginning of the formation.

Research funding. The list of topics of the European calls is more and more moving to applied research; in particular computer science is essentially considered as a tool for the applicative part in other research fields. The foundational research is funded essentially by the local government, every year with less funds since the economical crisis. What is the situation in the other countries outside of Europe?

Gender equality. Women are chronically underrepresented in the logic community; consequently they sometimes feel both conspicuous and isolated, and hence there is a risk that the underrepresentation is self-perpetuating. SIGLOG can play a role in order to attack such problems, by sponsoring public discussions and publishing statements about them. The best method would be to enhance collaboration with EACSL and EATCS. The three institutions could work together to enforce the role of logic both in teaching and research, and to influence the EU policy in financing the research projects in logic and foundation of Computer Science.

Candidate for Vice-Chair

Franck van Breugel
York University, Toronto, Canada

BIOGRAPHY

Academic Background:

Ph.D., Free University, Amsterdam, 1994, Computer Science.

Professional Experience:

Professor, York University, Toronto, Canada, 2009 – Present;
Leverhulme Visiting Professor, University of Oxford, UK, 2011 – 2012;
Faculty Fellow, Center for Advanced Studies, IBM, Toronto, Canada, 2003 – 2010.

Professional Interest:

Verification; Concurrency theory.

ACM Activities:

Program Committee, ACM ISSTA (tool demonstrations), 2014;
General Co-Chair, CONCUR (co-organized with ACM PODC), 2008;
Steering Committee, ACM PPDP, 1999 – 2004;
General Chair, ACM PPDP, 2000.

STATEMENT

At York University, which is the third largest university in Canada, I am currently the Chair of Senate. In 2017 and the first half of 2018, I was the Vice-Chair of Senate. Hence, I am well aware of how a Vice-Chair can support the Chair. Given that I have held several other significant administrative positions in the recent past, I believe that I am well equipped to take on the role of Vice-Chair of SIGLOG.

If I were to be elected as Vice-Chair, apart from supporting the Chair, I would like to focus on the following three activities with input and support from the SIGLOG community.

- Given that SIGLOG is only a few years old, I believe that it is essential that I actively promote SIGLOG among my colleagues and the computer science research community to increase membership.
- To diversify and, hence, strengthen the SIGLOG research community, I will actively seek avenues to recognize its young researchers, with a focus on those from underrepresented groups.
- Given my belief that logic provides tools, techniques, and insights that are invaluable to any undergraduate computer science student, I will advocate for the importance of logic in the undergraduate curriculum.

Candidate for Vice-Chair

Véronique Cortier
CNRS, Nancy, France

BIOGRAPHY

Academic Background:

Ph.D., ENS Cachan, 2003, Computer Science.

Professional Experience:

Research director, CNRS, Nancy, France, 2010 – Present;
Researcher, CNRS, Nancy, France, 2003 – 2010.

Professional Interest:

Security; Formal methods; Protocols; Voting; Cryptography.

ACM Activities:

SIGSAC Doctoral Dissertation Award Committee, 2018.

STATEMENT

SIGLOG is a young Special Interest Group while there is a large and active community working in logic and computation. I have been a member of the SIGLOG Executive Committee since its start in 2014 and I am pleased to be running for the Vice-Chair position, to help SIGLOG to grow and gather the community in logic and computation.

SIGLOG has been very active since its birth specifically, with the launch of an enjoyable and instructive newsletter and the creation of the Church Prize that awards outstanding contributions in logic and computation. There is already a large community in logic as shown by the strong conferences in the field such as LICS. If elected, I will work together with the Executive Committee members to make SIGLOG a strong and unified community that gathers and structures the existing community in logic and computation.

Candidate for Vice-Chair

Davide Sangiorgi
University of Bologna, Italy

BIOGRAPHY

Academic Background:

Ph.D., University of Edinburgh, 1993, Concurrency theory.

Professional Experience:

Full Professor, University Bologna, Italy, 2002 – Present;
Researcher, Senior Researcher, INRIA, Sophia Antipolis, France, 1995 – 2002;
Research Fellow, University of Edinburgh, United Kingdom, 1993 – 1995.

Professional Interest:

Concurrency theory; Type theory; Semantics (mainly operational); Programming languages;
Coinduction and related proof techniques.

Membership and Offices in Related Organizations:

Chair, WG 2.2 (Formal description of Programming Concepts), IFIP, 2004 – 2011;
Member, Academia Europaea, 2012 – Present;
Council Member, Italian Chapter of the European Association for Theoretical Computer Science,
2003 – 2010.

Awards Received:

LICS Test-of-Time Award, 2013;
IFIP Outstanding Service Award, 2012.

STATEMENT

I will try my best to assist the Chair, and to promote SIGLOG, with transparency and engagement, by relying on my experience in the international scenario, which is due both to the long periods spent in different countries, and to my scientific connections (including ties with with top conferences in the area).

Candidate for Vice-Chair

Peter Selinger
Dalhousie University, Halifax, Canada

BIOGRAPHY

Academic Background:

Ph.D., University of Pennsylvania, 1997, Mathematics.

Professional Experience:

Professor, Dalhousie University, Halifax, Canada, 2012 – Present;
Associate Professor, Dalhousie University, Halifax, Canada, 2005 – 2012;
Associate Professor, University of Ottawa, Canada, 2004 – 2005.

Professional Interest:

Programming languages; Quantum computing; Semantics; Quantum circuits; Type theory.

ACM Activities:

Program Committee, ACM SIGPLAN POPL, 2019;
Program Committee, ACM/IEEE LICS, 2018;
Program Committee, ACM/IEEE LICS, 2015.

STATEMENT

As SIGLOG Vice-Chair, my main role would be to serve on the steering committee and support the chair in their day-to-day activities and decision making.

Candidate for Secretary

Andrzej Murawski
University of Oxford, United Kingdom

BIOGRAPHY

Academic Background:

DPhil., University of Oxford, 2001, Computation.

Professional Experience:

Associate Professor, University of Oxford, UK, 2017 – Present;
Associate Professor, University of Warwick, Coventry, UK, 2013 – 2017;
Lecturer, University of Leicester, UK, 2010 – 2012.

Professional Interest:

Semantics of programming languages; Program verification; Game semantics;
Automata theory; Differential privacy.

ACM Activities:

Member, SIGLOG, 2010 - Present;
Member, SIGPLAN, 2018 – Present;
Editor, SIGLOG News, 2014 – Present;
Member, LICS Organizing Committee, 2010 – 2014.

Membership and Offices in Related Organizations:

Member, EATCS, Present;
Member, ETAPS Steering Committee, 2016 – 2017.

Awards Received:

Royal Society Leverhulme Trust Senior Research Fellowship, 2017;
EPSRC Advanced Research Fellowship, 2005;
ETAPS Best Theoretical Paper Award, 2005.

STATEMENT

It is an honour to run for the post of SIGLOG Secretary, through which I would like to contribute to the smooth running of the group and further growth of the SIGLOG community.

I see SIGLOG as a highly desirable initiative with a unique potential for bringing together logic-oriented researchers from across all continents and promoting logic-related research within computer science and beyond.

I am strongly committed to the idea of making SIGLOG an attractive platform for researchers from various subfields of logic and at various stages in their careers.

Candidate for Secretary

Elaine Pimentel
Universidade Federal do Rio Grande do Norte, Brazil

BIOGRAPHY

Academic Background:

Ph.D., Universidade Federal de Minas Gerais, 2001, Logic Applied to Computer Science.

Professional Experience:

Full Professor, Universidade Federal do Rio Grande do Norte, Brazil, 2018 – Present;
Associate Professor, UFRN/UFG, Brazil, 1994 – 2018;
Associate Professor, Universidad del Valle, Colombia, 2012.

Professional Interest:

Proof theory; Concurrency theory; Game semantics; Logical frameworks – calculus.

STATEMENT

We, as members of the ACM SIGLOG, are very fortunate to have had such an incredible leadership these past years. As Secretary of SIGLOG, I intend to continue Alexandra Silva's work in several aspects, as well as to propose some new initiatives.

First of all, I will maintain the fluent dialog with not only SIGLOG members but with our community in general. I intend to give special attention to conferences in Logic and Theoretical Computer Science since it seems to be very complicated, in principle, to get support from ACM for them. My goal is to try to ease this process and to strengthen the partnership between the ACM and the community of the above mentioned areas.

Second, I intend to continue enhancing the diversity of the community, with special attention to the 'Women in Logic' conference. I will also work towards enhancing the propaganda and influence of SIGLOG, specially in developing countries.

Finally, I intend to help support the implementation of disciplines in logic in the undergraduate computer science/mathematics curricula.

Candidate for Secretary

Alwen Fernanto Tiu
Australian National University, Canberra, Australia

BIOGRAPHY

Academic Background:

Ph.D., The Pennsylvania State University, 2004, Computer Science and Engineering.

Professional Experience:

Senior Lecturer, Australian National University, Canberra, Australia, 2013 – Present;
Assistant Professor, Nanyang Technological University, Singapore, 2013 – 2017;
Research Fellow, Australian National University, Canberra, Australia, 2006 – 2013.

Professional Interest:

Proof theory; Theorem proving; Concurrency theory; Computer Security;
Programming languages.

ACM Activities:

Associate Editor, ACM Transactions in Computational Logic, 2017 – Present.
Member, ACM, 2013 – Present;
Member, SIGLOG, 2016 – Present.

STATEMENT

It is an honour to be considered for nomination for an office in SIGLOG. I have been a member of ACM for a number of years and strongly support the role of ACM and SIGLOG in promoting research in logic and related areas. I have been actively involved in ACM related activities in the past several years: I served as a PC Chair of an ACM sponsored conference (CPP 2015), and am currently serving as an Associate Editor of ACM Transaction in Computational Logic. I am looking forward to engaging more in ACM sponsored events in the future and, if elected, will do my best to help promote ACM and SIGLOG, in particular in the Asia Pacific region.

Candidate for Treasurer

Amy Felty
University of Ottawa, Canada

BIOGRAPHY

Academic Background:

Ph.D., University of Pennsylvania, 1989, Computer Science.

Professional Experience:

Professor, School of Electrical Engineering and Computer Science, University of Ottawa, Canada, 2000 – Present;

Research Scientist, Computing Sciences Research Center, Bell Labs, Murray Hill, NJ, USA, 1991 – 1999;

Visiting Researcher, Inria, France, 1989 – 1991.

Professional Interest:

Theorem proving; Program verification; Formal methods; Logical frameworks; Systems biology.

ACM Activities:

Steering Committee Member, ACM SIGPLAN International Conference on Certified Programs and Proofs, 2018 – Present;

Program Co-Chair, ACM SIGPLAN International Conference on Certified Programs and Proofs, 2018;

Treasurer, ACM SIGLOG, 2016 – Present;

PC Member, ACM/IEEE Symposium on Logic in Computer Science, 2016.

Membership and Offices in Related Organizations:

Member of Editorial Board, Journal of Automated Reasoning, 2016 – Present;

Program Co-Chair, International Conference on Automated Deduction, 2015;

Steering Committee Member, International Conference on Interactive Theorem Proving, 2012 – Present.

STATEMENT

I am honored to have the opportunity to run for a second term as Treasurer of SIGLOG.

In addition to overseeing the general budget, I am proud to have supported some new SIGLOG initiatives including the Women in Logic (WiL) Workshop and several summer schools, which provide opportunities for students to participate in activities in the area of logic and computation. I would welcome the continued opportunity to support the growth and success of SIGLOG.

I strongly support the central goals of the organization as outlined in the founding documents, including advocating for the importance of logic in the undergraduate computer science curriculum and establishing prizes that recognize the significant contributions of members of our community.

In addition to serving one term as SIGLOG Treasurer, I have a broad range of professional experience that I bring to this role. This experience includes serving as PC chair, steering committee member, and PC member of numerous conferences in logical foundations of computer science, formal methods, and programming languages.

Candidate for Treasurer

Vivek Nigam
fortiss GmbH, Munich, Germany

BIOGRAPHY

Academic Background:

Ph.D., École Polytechnique, 2009, Computational Logic.

Professional Experience:

Researcher, fortiss GmbH, Munich, Germany, 2017 – Present;
Assistant Professor, Federal University of Paraíba, João Pessoa, Brazil, 2012 – Present;
Alexander von Humboldt Post-Doctoral Researcher, Ludwig-Maximilians-Universität, Munich, Germany, 2010 – 2012.

Professional Interest:

Computational Logic; Foundations of Computer Security; Safety and Security;
Proof Theory; Denotational Semantics.

ACM Activities:

Programme Committee, CPP, 2018;
Programme Committee, PPDP, 2017;
External Review Committee, POPL, 2017;
Programme Committee, LICS, 2016.

STATEMENT

Research is all about crossing bridges. This is a motto that I have used during my whole research career. To be directly involved in a body such as the SIGLOG, as SIGLOG Treasurer, would be a great honour. If elected, my goal in SIGLOG will be, as in my research career, to further connect the logic community to other communities.

I truly believe that the foundational principles provided by logic has a great positive impact on the development and maintenance of computational systems. Indeed, logic based tools and methods have been heavily used in non-academic projects with great success. If elected as a SIGLOG executive, it will be my obsession to increase even further the interaction of the logic community with industry and other communities. Since 2017 I have been working in fortiss GmbH a transfer institute in Germany and I have had the opportunity to see logic being used in the 'real-world'. Besides the positive impact on existing practices, this interaction also provides insights to new logical problems, providing motivation and an extra excitement boost, in particular, to the logic students.

Candidate for Treasurer

R Ramanujam
Institute of Mathematical Sciences, Chennai, India

BIOGRAPHY

Academic Background:

Ph.D., Tata Institute of Fundamental Research, Mumbai, 1988 Computer Science.

Professional Experience:

Faculty, Institute of Mathematical Sciences, Chennai, India, 1988 – Present.

Professional Interest:

Logic in computer science; Automata theory; Theory of distributed systems; Game theory; Security theory.

ACM Activities:

Member, SIGLOG Education Committee, 2016 – Present;

Member, Executive Committee, Chennai Chapter, 2011 – 2016.

Membership and Offices in Related Organizations:

Member, ASL East Asia committee, 2013 – Present;

Member, ASL Executive Council, 2010 – 2014.

STATEMENT

I am honoured and excited to consider the possibility of contributing significantly to ACM SIGLOG. I have been active in the Indian Theoretical Computer Science and Logic communities, in setting up and running the Association for Logic in India, and also in the international community of logicians via the ASL. I have been active in mathematics and logic education in India at different levels over a couple of decades.

With all of this experience, I see possibilities for expanding the SIGLOG community's networking not only among different professional groups such as mathematical and philosophical logicians, AI researchers and system verification researchers, but also diversify the community significantly across the world. As a non-US non-European researcher (with teaching experience in India and China) I can see the potential of SIGLOG in Asia and elsewhere. Another major area of concern is gender. If elected, I would like to prioritize expansion of the community and its education.

Candidate for Treasurer

Giselle Reis
Carnegie Mellon University, Doha, Qatar

BIOGRAPHY

Academic Background:

Ph.D., TU Wien, 2014, Logic.

Professional Experience:

Assistant Professor, Carnegie Mellon University, Doha, Qatar, 2016 – Present;
Post-doc, Inria, France, 2014 – 2016;

Professional Interest:

Proof theory; Logical frameworks; Theorem proving.

ACM Activities:

Program Committee, Women in Logic, 2019;
Program Committee, ACML LICS, 2018;
Member, ACM & SIGCSE, 2018 – Present.

Membership and Offices in Related Organizations:

Workshop Co-Chair, PxTP, 2019;
Steering Committee, LFMTTP, 2018 – Present;
Workshop Co-Chair, LFMTTP, 2018.

STATEMENT

It is an honor to be considered for the position of Treasurer of SIGLOG. As I got to know more about SIGLOG and its activities, I came to appreciate its role in building and supporting a community of logicians in computer science. It feels reassuring to have an organization that aggregates and disseminates knowledge in the area (through its website and newsletters), sponsors conferences and events, and recognizes the important contributions to the field. For these reasons, I would be happy to serve as SIGLOG's Treasurer, and give something back to the community.

If elected, I will continue the support of SIGLOG's finances by preparing the annual budget, monitoring expenses and reimbursements, and issuing reports. Moreover, I will participate in all necessary meetings and I am ready to implement any changes/improvements to the processes that the board deems necessary.