

Candidate for Chair

Yiran Chen
Duke University, Durham, NC, USA

BIOGRAPHY

Academic Background:

Ph.D., Purdue University, 2005 Electrical and Computer Engineering.

Professional Experience:

Professor, Duke University, Durham, NC, 2019 – Present;
Associate Professor, Duke University, Durham, NC, 2017 – 2019;
Associate Professor, University of Pittsburgh, PA, 2014 – 2017.

Professional Interest:

Neuromorphic computing; Machine learning acceleration; Emerging memory and storage; Mobile and edge computing, Security and privacy of machine learning systems.

ACM Activities:

Technical Activity Chair, SIGDA, 2018 – Present;
Education Chair, SIGDA, 2015 – 2018;
Associate Editor, ACM TODAES, 2020 – Present;
General Chair, ACM GLSVLSI, 2021 – Present.

Membership and Offices in Related Organizations:

Fellow, ACM, 2012 – Present;
Fellow, IEEE, 2005 – Present;
Executive Committee Member, SIGDA, 2012 – Present;

Awards Received:

Fellow of ACM, 2020;
Fellow of IEEE, 2018;
ACM SIGDA Service Award, 2016;
ACM SIGDA Outstanding New Faculty Award, 2014.

STATEMENT

My academic career is deeply associated with ACM SIGDA. As a long-time SIGDA volunteer, I have served as the organizers of many SIGDA activities and conferences, and associate editors of several ACM periodicals. I also served SIGDA EC for two terms (2015-2018 and 2018-2021) as Education Chair and Technical Activity Chair, respectively. In my 1st term, I was responsible for coordinating all of SIGDA's educational activities and initiated the first ACM SIGDA global educational partnership

with Cadence. In my 2nd term, I established the industrial advisory board of SIGDA and created Design Automation WebiNar (DAWN) together with IEEE CEDA during the COVID-19 pandemic.

SIGDA is facing many challenges: the rising of emerging technologies offers many opportunities while also dilutes the efforts on traditional research; COVID-19 pandemic hinders international collaborations and puts the sustainability of many conferences and activities in danger; the new publication models like open-access and arXiv dramatically change research and publication patterns. If elected as chair, I will promote the SIGDA's global impacts and vitalize EDA technologies and community by encouraging the engagement with new applications and industries, the excellence of community services, and the educations of young generations and underrepresented groups.

Candidate for Chair

Yiyu Shi
University of Notre Dame, IN, USA

BIOGRAPHY

Academic Background:

Ph.D., University of California, Los Angeles, 2009, Electrical Engineering.

Professional Experience:

Visiting Scientist, Boston Children's Hospital/Harvard Medical School, Boston, MA,
2020 – Present;

Associate Professor, University of Notre Dame, IN, 2015 – Present;

Assistant Professor, Missouri University of Science and Technology, Rolla, MO, 2010 – 2015.

Professional Interest:

Machine Learning in Medicine, Hardware acceleration of deep learning, On-device learning.

ACM Activities:

Education Chair, SIGDA, 2017 – Present.

Awards Received:

Top Winning Award, IEEE SERVICES Hackathon, 2020;

IEEE Computer Society TCVLSI Mid-Career Achievement Award, 2019;

ACM SIGDA Outstanding Service Award, 2015;

IEEE Region 5 Outstanding Individual Achievement Award, 2015.

STATEMENT

Over the past few years, I have actively participated in a number of ACM and/or SIGDA activities. I currently serve as the education chair of SIGDA, as an associate editor of the ACM SIGDA Newsletter and JETC, as well as the chair of the SIGDA Ph.D. Forum at DAC and the ACM Student Research Competition at ICCAD. I also proudly initiated and chaired two SIGDA events, SIGDA Live and Hardware Design Contest at DAC. I have the vision to further enhance the impact of SIGDA, both laterally and longitudinally. First, SIGDA now has an active presence in US and Europe, but not as much in Asia. I want to promote SIGDA in these regions by sponsoring various activities including seminars, contests, workshops and/or conferences that would address the need of local EDA communities, and to help them stay connected and informed. Second, the traditional EDA community is now rapidly expanding into emerging areas such as automobile, security, machine learning, etc. SIGDA can and should help EDA researchers, especially the young generation in fostering their growth and long-term career in these areas. I plan to organize dedicated workshops and events through SIGDA towards this.

Candidate for Executive Committee

Aida Todri-Sanial
CNRS, LIRMM, Montpellier, France

BIOGRAPHY

Academic Background:

Ph.D., University of California Santa Barbara, 2009, Electrical and Computer Engineering.

Professional Experience:

Director of Research, CNRS, LIRMM, Montpellier, France, 2011 – Present;
Visiting Research Fellow, University of Cambridge, Cambridge, UK, 2016 – 2017.

Professional Interest:

Energy efficient computing, Euromorphic computing, Quantum computing, Physical modeling & simulation, Design automation.

ACM Activities:

Guest Editor, ACM JETC, 2020 – Present;
Guest Editor, ACM JETC, 2017 – 2018;
Guest Editor, ACM JETC, 2015 – 2016.

Membership and Offices in Related Organizations:

Co-Editor in Chief E-Newsletter, ACM SIGDA, 2016 – 2019.

Awards Received:

ACM SIGDA Meritorious Service Award, 2020;
Inspirational Women of the Year, University of Montpellier, France, 2019;
Franco-British Young Leader, 2018;
CNRS Bronze Medal for Research & Innovation, 2016.

STATEMENT

I am honored to be nominated as a candidate for a position on the SIGDA Executive Committee. I have been a member and active ACM volunteer for over five years now and have been contributing to the success of SIGDA since 2016. I had the opportunity to volunteer as co-Editor-in-Chief for ACM SIGDA's e-newsletter from 2016-2019 and I also regularly serve as a committee member on various ACM conferences and journals.

SIGDA has made a significant impact over the past years by adding novel and dynamic mechanisms to attract new members. SIGDA must continue to develop its strategy to encourage growth in new regions while providing clear benefits to existing members. I will bring my experience and vision in engaging with the community to highlight women scientists in SIGDA and diverse young professionals. Also, I will propose novel transversal actions between different ACM communities on topics such as novel computing paradigm (AI, neuromorphic and quantum computing) where Design Automation tools are essential for their design and building large scale systems. If elected to the Executive Committee, I will work with each active group member to ensure that ACM SIGDA remains the leading Special Interest Group on applied computing.

Candidate for Executive Committee

Jingtong Hu
University of Pittsburgh, PA, USA

BIOGRAPHY

Academic Background:

Ph.D., University of Texas at Dallas, 2013, Computer Science.

Professional Experience:

Associate Professor, University of Pittsburgh, 2020 – Present;

Assistant Professor, University of Pittsburgh, 2017 – 2020;

Assistant Professor, Oklahoma State University, Stillwater, 2013 – 2017.

Professional Interest:

FPGA, Embedded System, Machine Learning, Design Automation, Energy Harvesting.

ACM Activities:

TPC Track Chair, Design Automation Conference 2019, SIGDA;

Chair, System Design Contest at DAC, SIGDA, 2018;

Chair, SIGDA Student Research Forum at ASP-DAC 2019, SIGDA;

Chair, SIGDA's CADathlon 2017 Contest at ICCAD, SIGDA, 2017.

Membership and Offices in Related Organizations:

Member, SIGDA, Present;

Member, SIGBED, Present;

Member, SIGSAC, Present.

Awards Received:

SIGDA Meritorious Service Award, 2019.

STATEMENT

It is my great pleasure to run for the SIGDA Executive Committee. I have been a member and active ACM volunteer for almost a decade. In recent years, I have co-founded the System Design Contest at DAC and served as the Chair/Co-Chair. I have chaired SIGDA's student research forum at ASP-DAC 2018. I have also served as the Chair/Co-Chair for CADathlon contest from 2014-2017. Additionally, I have served as TPC Track Chair for both ASP-DAC and DAC, publicity chair for GLSVLSI, ISVLSI, etc. I have gratefully received the 2019 SIGDA Meritorious Service Award. My experience in these varied positions has prepared me well for serving you.

ACM SIGDA has made significant impacts over the past decades by working closely with industrial partners and academic members. SIGDA must continue to develop to better serve existing community members and to encourage growth in new regions and new areas. This requires better connection with existing members and partners as well as understanding of new trends. If elected as part of the EC, I will connect with every SIGDA member through various activities and make greater positive impacts to everyone's careers.

Candidate for Executive Committee

Laleh Behjat
University of Calgary, Canada

BIOGRAPHY

Academic Background:

Ph.D., University of Waterloo, 2002, Electrical and Computer Engineering.

Professional Experience:

Professor, University of Calgary, Canada, 2017 – Present;
Adjunct Professor, Universidade Federal Do Rio Grande Do Sul, Porte Alegre, Brazil, 2017 – 2019;
Associate Professor, University of Calgary, Canada, 2012 – 2017.

Professional Interest:

Physical Design, Place and route, Mathematical optimization, Machine Learning Engineering and computer science education.

ACM Activities:

Associate Editor, Transactions on Design Automation of Electronic Systems, 2020 – Present;
Technical Program Chair, International Symposium on Physical Design, 2020 – Present;
Member, SIGDA Diversity Committee, 2019 – Present;
ACM Recognition Service Award, 2017.

Membership and Offices in Related Organizations:

Associate Editor, IEEE Transactions on Computer-Aided Design, 2016 – Present;
Member, IEEE Teaching Awards Committee, 2019 – Present;
Co-chair, Young Fellows Program at the Design Automation Conference, 2016 – 2020.

Awards Received:

NSERC Chair for Women in Science and Engineering-Prairie Region, 2020.
ASTech Leadership Excellence in Science and Technology, 2017;
1st Place - ISPD Routability Placement Award, 2014.

STATEMENT

I am an established researcher in EDA with interdisciplinary collaborations. I have contributed to SIGDA through my research and collaborations in physical design, organizing student activities and conferences, as an associate editor of TODAES, and championing women and other underrepresented groups.

SIGDA is a welcoming and inclusive environment. I will continue this tradition and find ways to complement the current efforts by focusing on the professional development of the next generation, facilitating collaborations between academics and industry leaders, and championing women and other underrepresented groups.

Professional development: For the past decade, I have been working to enhance, enrich and transform the experience of young professionals in EDA. I have developed non-traditional learning opportunities, co-chaired the Young Fellows Program at DAC and provided several learning and networking opportunities.

Academic and industry collaborations: Advances in AI will change the industry in the next decade. EDA researchers are well situated to be the leaders. We will need better collaboration between academics and industry to ensure that EDA researchers benefit from these changes.

Equity, Diversity, and Inclusion: Diversity and inclusivity have shown to improve productivity and efficiency. I am an advocate of diversity and will work to build an inclusive environment for all.

Candidate for Executive Committee

Matthew Richard Guthaus
University of California Santa Cruz, CA, USA

BIOGRAPHY

Academic Background:

Ph.D., University of Michigan, 2006, Electrical Engineering.

Professional Experience:

Asst./Assoc./Full Professor, University of California Santa Cruz, CA, 2006 – Present;
Supplemental Researcher, IBM Research, Yorktown Heights, NY, 2003 – 2005.

Professional Interest:

Electronic Design Automation (EDA), Low-Power Chip Design (VLSI), Memory Circuits and Design Automation, Clocking Circuits and Design Automation, Open-Source Software.

ACM Activities:

Executive Committee (Finance), SIGDA, 2012 – 2015;
Advisory Board, SIGDA, 2008 – 2012;
E-Newsletter Editor, SIGDA, 2006 – 2012;
CADathlon Chair/Liaison, SIGDA, 2006 – 2009.

Membership and Offices in Related Organizations:

Vice Chair, IEEE Monterey Bay, 2008 – 2010;
Co-Chair, Workshop on Open-Source EDA Technologies, 2015;
Member and VLSI-SoC Steering Committee, IFIP WG10.5, 2013 – Present.

Awards Received:

ACM SIGDA Service Award, 2015;
ACM SIGDA Distinguished Service Award, 2010;
ACM SIGDA Service Award, 2009;
ACM SIGDA CADathlon First Place (2002 and 2005), 2005.

STATEMENT

ACM SIGDA needs to focus on access to information, inspiration at all stages of careers, and interpersonal connections. To do this, we must pique the interest of new members and reinvigorate our existing member base. It is particularly important to encourage international and underrepresented participation as we are a small niche field compared to others. Inclusivity is not only necessary but a solution for EDA in the future. There are several ways that SIGDA can start (or continue) forging down this

path: providing online seminars and workshops, publishing open access papers and journals, releasing open-source EDA tools and benchmarks, and running hands-on "maker" style workshops and tutorials for a general audience. We are seeing a new era of hardware at the board and system level, so it is important that SIGDA be included and not left behind this movement. Accessible resources should be a gateway into our conferences where we can offer affordable in-person and virtual options to expand participation.

Candidate for Executive Committee

Muhammad Shafique
New York University Abu Dhabi (NYUAD), Abu Dhabi, UAE

BIOGRAPHY

Academic Background:

Ph.D. (magna cum laude), Karlsruhe Institute of Technology (KIT), 2011, Computer Science.

Professional Experience:

Associate Professor, New York University Abu Dhabi (NYUAD), New York University (NYU), UAE, USA, 2020 – Present;

Full Professor, Vienna University of Technology (TU Wien), Vienna, Austria, 2016 – 2020;

Research Group Leader, Karlsruhe Institute of Technology (KIT), Karlsruhe, Germany, 2011 – 2016.

Professional Interest:

Brain-inspired Computing & Embedded Machine Learning/AI Systems, Low-Power, Energy-Efficient and Robust Computing, AI for Wearable Healthcare, Fault-Tolerant and Secure Hardware, Embedded Autonomous Systems for CPS and IoT.

ACM Activities:

Associate Editor, ACM Transactions on Embedded Computing (TECS), 2020 – Present;

Organizer, ACM SIGDA Design Automation Summer School, 2017 – 2019;

Special Session Organizer, DAC, DATE, ICCAD, EMSOFT Conferences, 2014 – 2021;

Workshop Organizer, DATE, ICCAD, ESWeek, 2014 – 2018.

Membership and Offices in Related Organizations:

Member, ACM, SIGDA, SIGARCH, and SIGBED, 2017 – Present.

Awards Received:

AI-2000 Chip Technology Most Influential Scholar Award, 2020;

ACM SIGDA Outstanding New Faculty Award, 2015;

Best Paper Award Nominations at DAC 2017, DATE 2017, and DAC 2017;

Best Paper Award at CODES+ISSS 2015, 2014, and 2011.

STATEMENT

I am honored to have been asked to run for the SIGDA Executive Committee election. I actively contributed to the ACM and SIGDA communities through various means like

organizing the summer school at DAC and special sessions/workshops, TPC services, track chair, top-quality publications, etc. My works at DAC, ICCAD, DATE, and CODES+ISSS received best paper awards and nominations. In recognition of my scientific record/services in top-EDA conferences, I received the AI-2000 Chip Technology Most Influential Scholar Award in 2020 and the 2015 SIGDA Outstanding New Faculty Award.

With the evolution of AI/ML, the role of the EDA community has become even more important to develop energy-efficient, secure, and intelligent/autonomous systems through automated methods/tools. My cross-disciplinary expertise and international contacts across diverse fields will greatly contribute to the SIGDA community. Having said that, the focus of SIGDA is at the core of my research interests. Considering my experience, passion for giving back to the community, and my ambitions to fuel the evolution of the SIGDA community with new directions and ideas, I would be highly interested in playing an important role in organizing and supporting the activities to promote research and education in EDA.

Candidate for Executive Committee

Preeti Ranjan Panda
IIT Delhi, New Delhi, India

BIOGRAPHY

Academic Background:

Ph.D., University of California at Irvine, 1998, Embedded Systems.

Professional Experience:

Head, Khosla School of Information Technology, IIT Delhi, New Delhi, India,
2018 – Present;

Professor, Dept. of CSE, IIT Delhi, New Delhi, India, 2010 – Present;

Staff R&D Engineer, Synopsys, Inc., Mountain View, CA, USA, 1998 – 2002.

Professional Interest:

Embedded systems, Energy efficient computing, Electronic design automation,
Embedded memory, VLSI synthesis.

ACM Activities:

Program Co-Chair, IEEE/ACM CASES Conference, SIGDA, 2021;

Program Co-Chair, IEEE/ACM CODES+ISSS Conference, SIGDA, 2013;

Committee Member, ACM SIGDA Outstanding PhD Dissertation Award, SIGDA, 2015;

Associate Editor, ACM TODAES, SIGDA, 2008 – 2011.

Membership and Offices in Related Organizations:

Editor-in-Chief, IEEE Embedded Systems Letters, IEEE, 2020 – Present;

Senior member, IEEE, 2010 – Present;

Associate Editor, IEEE TCAD, 2018 – Present.

Awards Received:

IESA Techno-mentor Award, 2019;

IBM Faculty Award, 2007;

Young Scientist Award, 2003;

Honorable Mention Award, VLSI Design 1992.

STATEMENT

I would like to contribute to SIGDA as an Executive Committee member. I am a Professor in CSE and Head of the Khosla School of IT at IIT Delhi. I have previously worked at Texas Instruments and Synopsys. My research interests cover embedded systems, design automation, memory technologies, and power-efficient design. I have

served the design automation community as Editor-in-Chief of IEEE Embedded Systems Letters; Technical Program Co-Chair of CODES+ISSS; associate editor of IEEE TCAD and ACM TODAES; steering committee member of ASPDAC; Best PhD thesis/paper award committees of ACM SIGDA and DAC; and Program Committee member of DAC, ICCAD, DATE, etc.

If elected, I will contribute to the growth of SIGDA's footprint in several ways:

- Expand audience involvement in SIGDA events by promoting the transition to hybrid/online modes.
- Introduce new student researcher oriented online events.
- Broaden the outreach of SIGDA in geographical areas such as India and Asia, that have demonstrated increased activity in hardware and design automation areas in recent years.
- Improve the interaction and technical collaboration with other ACM SIGs, such as SIGBED.

It would be my honour to serve as an Executive Committee member of SIGDA.

Candidate for Executive Committee

Rajsaktish Sankaranarayanan
Intel Corporation, San Jose, CA, USA

BIOGRAPHY

Academic Background:

Ph.D., University of California Santa Cruz, 2017, Computer Engineering.

Professional Experience:

System on Chip (SoC) Design Engineer, Intel Corporation, San Jose, CA,
2016 – Present;

Applications Engineer, Transmeta Corporation, Santa Clara, CA, 2008– 2009;
Applications Engineer, Veritools Incorporated, Los Altos, CA, 2004 – 2008.

Professional Interest:

Design automation, Low power design, 2D/2.5D/3D silicon integration, Beyond CMOS technologies.

ACM Activities:

Associate Editor, SIGDA, 2018 – Present.

Membership and Offices in Related Organizations:

Senior Member, IEEE, 2010 – Present;

Member, ACM, 2012 – Present;

Secretary, IEEE Computer Society Santa Clara Valley Chapter, 2019 – 2020.

STATEMENT

I feel honored at this opportunity to run for office of Executive Committee. I have been a member of ACM and IEEE for several years and have been an active member. My involvement has been primarily as researcher and participant at SIGDA sponsored events and as Registration Chair for VLSI-SoC 2012. In addition, I have served as volunteer and Secretary of the IEEE Computer Society, Santa Clara Valley Chapter. Since 2018, I have been serving as Associate Editor for SIGDA E-newsletter contributing to a column series titled “Researcher spotlight”. Through these positions, I have gained experience to prepare me to serve ACM SIGDA as Executive Committee Member.

As member of the SIGDA Executive Committee I would like to:

- Promote stronger collaboration between industry and academic partners.

- Increase industry participation to provide opportunities for the student participation in SIGDA events.
- Find ideas and support ones that can make a significant impact to SIGDA's charter.
- Add value to existing membership and communicate effectively.
- Widen partnerships and demonstrated presence through partnership with other SIGs and Conferences, both of which are evolving with emerging technologies.
- Periodically evaluate membership strength, industry academia proportions, attrition rates and develop strategies to manage the same.

Candidate for Executive Committee

Sudeep Pasricha
Colorado State University, Fort Collins, CO

BIOGRAPHY

Academic Background:

Ph.D., University of California, Irvine, 2008, Computer Science.

Professional Experience:

Professor, Colorado State University, Fort Collins, CO, USA, 2019 – Present;
Associate Professor, Colorado State University, Fort Collins, CO, USA, 2014 – 2019;
Assistant Professor, Colorado State University, Fort Collins, CO, USA, 2008 – 2014.

Professional Interest:

Embedded and IoT Systems, Design Automation with Emerging Technologies,
Chip-scale Network and Memory Subsystems, Hardware/Software Co-design,
Energy-efficient, Fault-tolerant, Secure, Real-time Computing.

ACM Activities:

General Chair, ACM International Symposium on Networks-on-Chip (NOCS),
2019 – Present;
Senior Associate Editor, ACM Journal on Emerging Technologies in Computing Systems
(JETC), 2020 – Present;
Chair, ACM SIGDA Ph.D. Forum, 2017 – 2018;
Editor-in-Chief, ACM SIGDA E-News, 2012 – 2016.

Membership and Offices in Related Organizations:

Steering Committee, IEEE Transactions on Sustainable Computing (TSUSC),
2020 – Present;
Activities Committee, IEEE Council on Electronic Design Automation, 2017 – 2018;
Senior Member, IEEE, 2013 – Present.

Awards Received:

ACM SIGDA Distinguished Service Award, 2019;
ACM SIGDA Service Award, 2015;
AFOSR Young Investigator Award, 2013;
ACM SIGDA Technical Leadership Award, 2012.

STATEMENT

SIGDA is an organization that is very dear to me because it is the voice of the design automation community of which I am a part of, and because of its initiatives designed to support students, which I personally have benefited from. It has given me great pleasure to lead and organize various flagship SIGDA initiatives over the past decade: SIGDA CADathlon (2009-2012), SIGDA E-News (2012-2016), SIGDA University Demo (2016-2017), and the SIGDA PhD Forum (2017-2018).

I am now seeking to volunteer on the ACM SIGDA Executive Committee (EC). As part of the SIGDA EC, I will use my position to expand student grants and support initiatives, improve recognition of academic/industry experts and the under-appreciated volunteers in the SIGDA community, and dramatically enhance publicity and web presence. I will initiate new workshops, online seminars, and meaningful social media platforms to bring SIGDA community members closer together. I also will work hard towards improving the profile of SIGDA with funding agencies, to ensure sustained growth and a vibrant community that evolves with the changing technology landscape.

Candidate for Executive Committee

Wanli Chang
University of York, UK

BIOGRAPHY

Academic Background:

Ph.D., Technical University of Munich, 2017, Computer Engineering.

Professional Experience:

Permanent Academic Staff, University of York, UK, 2018 – Present.

Professional Interest:

Design automation, Embedded systems, Real-time systems, Cyber-physical systems, Optimization.

ACM Activities:

Associate Editor of Newsletter, SIGDA, 2020 – Present;
Chair of Autonomous Systems in DAC, SIGDA, 2020 – Present;
PC Member of EMSOFT, SIGBED, 2019 – 2020;
Best Paper Award Committee Member of CODES+ISSS, SIGBED, 2020.

Membership and Offices in Related Organizations:

Young Professionals Committee, IEEE CEDA, 2020 – Present;
Industry Track Founding Chair of RTAS, IEEE TCRTS, 2020 – 2021;
Best Paper Award Committee Member of RTSS, IEEE TCRTS, 2019.

Awards Received:

Best Paper Award Candidate, EMSOFT, 2020
Best Paper Nomination, RTAS, 2020
Best Paper Nomination Award, DATE, 2019
Best Dissertation Award, Technical University of Munich, Germany, 2018.

STATEMENT

As motivated by practical applications in the industry, including autonomous vehicles, robotics, and industry automation, the field of embedded systems and their design automation is moving towards high performance and complex functionalities, requiring new theories and technologies. I have been devoted to this research direction and published 15 papers in relevant top conferences listed by csrcranking in the past two years.

I will dedicate myself towards two main goals. First, I will strengthen the relationship between academia and industry. There are practical problems to which the industry is looking for solutions and that the academics can solve. I have successfully founded and chaired the industry track of RTAS 2021, the first of its kind in a top real-time and embedded systems conference. Most major companies in the field of embedded systems and their automation around the world have been attracted. Second, I will seek support from senior academics and create more career development opportunities for the young professionals in the area. In general, we have not been able to get positions in the top schools. This needs to be changed on the ACM SIG level.

Thank you for voting for me and please do not hesitate to contact me for any suggestions!

Candidate for Executive Committee

Yuan-Hao Chang
Academia Sinica, Taipei, Taiwan

BIOGRAPHY

Academic Background:

Ph.D., National Taiwan University, 2009, Computer Science.

Professional Experience:

Jointly Appointed Professor, National Taiwan University, Taipei, Taiwan,
2020 – Present;

Deputy Director of Institute of Information Science, Academia Sinica, Taipei, Taiwan,
2019 – Present;

Research Fellow, Academia Sinica, Taipei, Taiwan, 2018 – Present.

Professional Interest:

Real-time Systems, Embedded Systems, Computer Systems, HW-SW Codesign,
Design Automation.

ACM Activities:

Associate Editor, ACM Transactions on Cyber-Physical Systems, 2018 – Present;

Column Editor, "What is" Column in ACM SIGDA E-newsletter, 2018 – 2019;

Publicity Chair, ACM GLSVLSI, 2018;

Local Co-Chair, ACM/IEEE ISLPED, 2017.

Membership and Offices in Related Organizations:

Associate Editor, IEEE Transactions on Emerging Topics in Computing (TETC),
2019 – Present;

Steering Committee Member, IEEE NVMSA, 2018 – Present;

Young Professionals Affinity Group Chair, IEEE Taipei Section, 2016 – 2017.

Awards Received:

Best Paper Award from ACM/IEEE ISLPED 2020;

GLSVLSI Service Recognition Award from ACM GLSVLSI, 2020;

Best Paper Award from ACM/IEEE CODES+ISSS 2019;

Best Paper Award from IEEE NVMSA 2019.

STATEMENT

I am honored to have the chance to run for a position on SIGDA's Executive Committee.
I have been a member and active ACM volunteer for more than 10 years. I also serve

as a program committee member on various ACM conferences (including DAC and ASP-DAC) and an associate editor of two ACM Transactions. ACM SIGDA has made a significant impact over the past decade and must continue to grow in new regions and provide benefits to existing members. Meanwhile, we need to continue to promote and improve SIGDA's ability to attract and support new members. If elected as an Executive Committee Member, I will work with the Chair and other committee members to make ACM SIGDA remain the leading interest group on design automation.

Candidate for Executive Committee

Yu Wang
Tsinghua University, Beijing, China,

BIOGRAPHY

Academic Background:

Ph.D., Tsinghua University, 2007, Electronic Engineering.

Professional Experience:

Chair of the Department of Electronic Engineering, Tsinghua University, Beijing, China, 2020 – Present;

Dean of Institute for Electronics and Information Technology in Tianjin, Tsinghua University, Beijing, China, 2020 – Present;

Professor (Assistant/Tenured Associate/Tenured Full), Tsinghua University, Beijing, China, 2007 – Present.

Professional Interest:

Energy Efficient Circuits and Systems, Electronic Design Automation, Field-Programmable Gate Array, VLSI Design, Embedded Systems.

ACM Activities:

Co-Editor-in-Chief, ACM SIGDA E-News, 2017 – 2019;

General Chair Secretary, ASP-DAC Conference, 2019;

Executive Committee Member, DAC Conference, 2020 – Present;

Technical Program Committee Member, FPGA Conference, 2014 – 2016.

Membership and Offices in Related Organizations:

Associate Editor, IEEE TCAD, 2013 – Present;

Track Chair, DATE Conference, 2017 – 2019;

Technical Program Committee Chair, IEEE Computer Society Annual Symposium on VLSI (ISVLSI), 2018;

Awards Received:

Best Paper Award, SIGDA ASP-DAC, 2019;

ACM/SIGDA DAC Under-40 Innovators Award, 2018;

Best Paper Award, ACM/SIGDA FPGA, 2017;

STATEMENT

I will put efforts into the following issues if elected:

- (1) Promoting research of emerging techniques in the area of design automation. My team is one of the earliest groups on embedded FPGA-based deep learning accelerator, ReRAM-based PIM architecture, etc. This research has been approved by peers and my group has won several awards. I'll keep promoting the research of these areas.
- (2) Building up intimate connections between academia and industry. SIGDA plays an important role in integrating resources from both academia and industry. As one of the co-founders of DeePhi Tech (now acquired by Xilinx) and a professor at Tsinghua University, I will keep building up such connections.
- (3) Strengthening the influence of SIGDA in the Asia-Pacific region. The Asia-Pacific region contributes to 2/3 population and 1/3 GDP of the world. Attracting talents from the Asia-Pacific region is important. I was the only scholar from mainland China invited to the SIGDA Technical Committee of Reconfigurable Computing in 2016, and I am a committee member of the SIGDA Northern China Chapter. I believe such roles will be helpful in promoting the influence of SIGDA in the Asia Pacific region.